

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,....

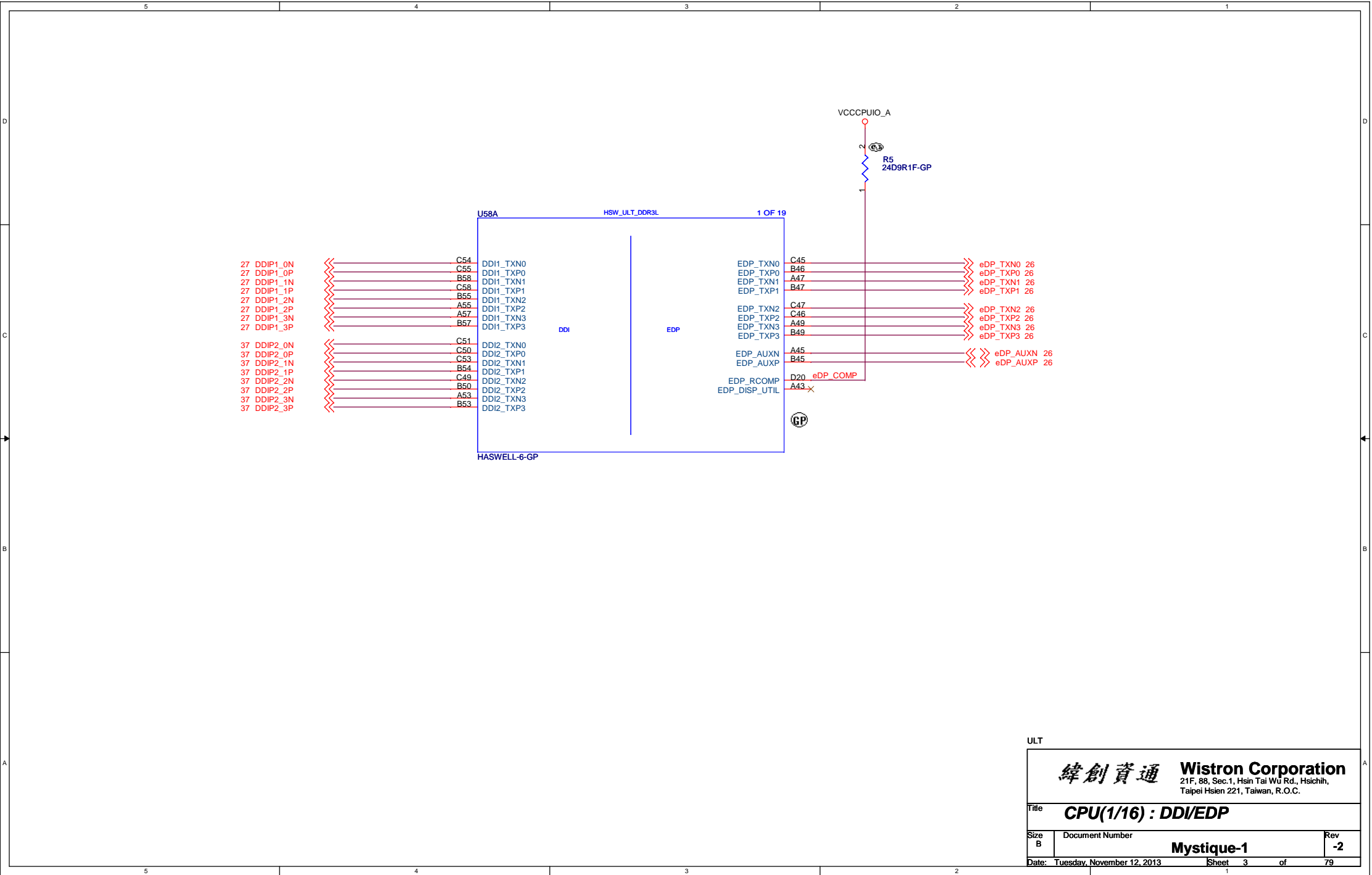
CAPACITOR

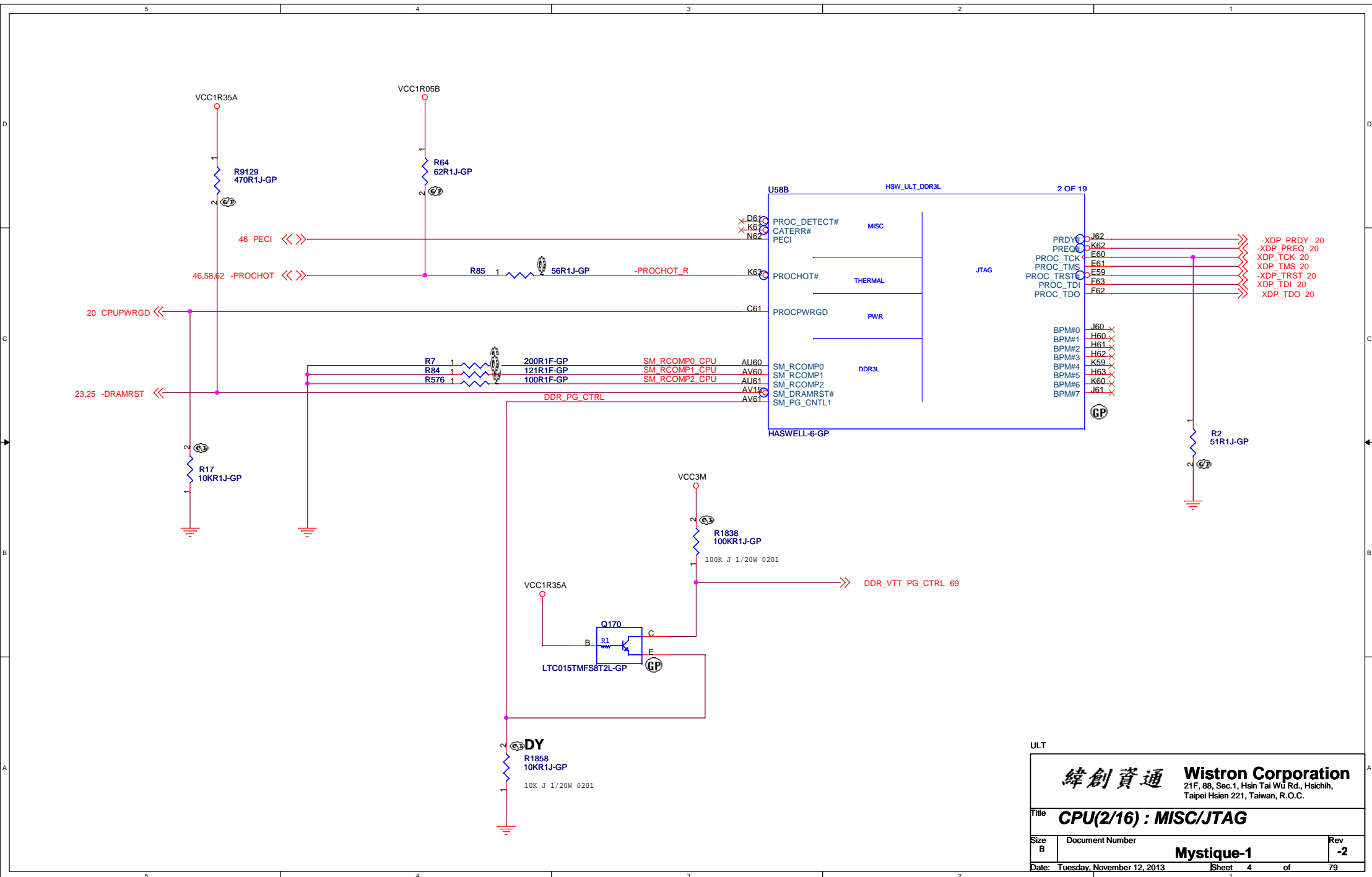
Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

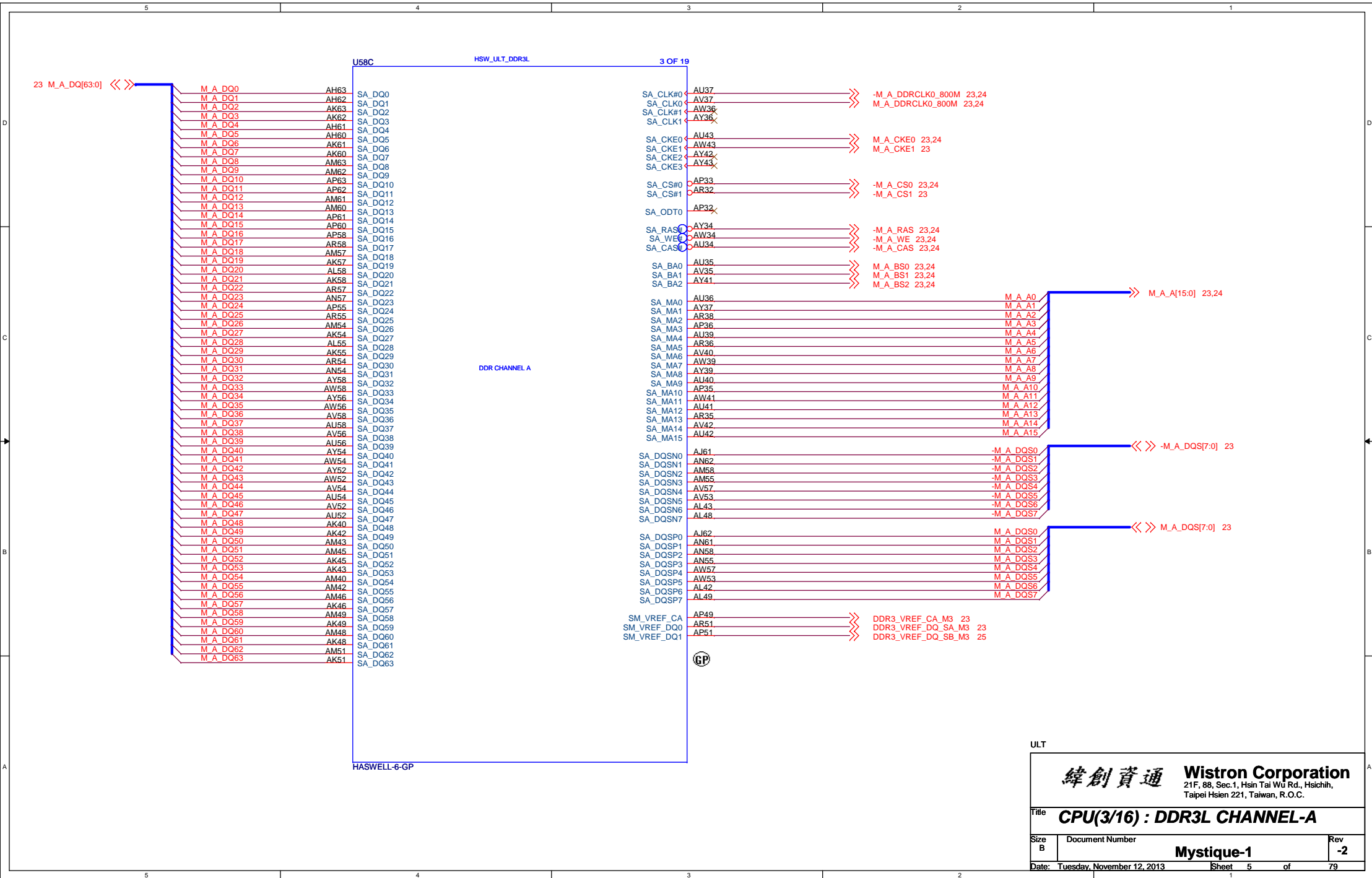
ULT

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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		EC HISTORY	
Size C	Document Number	Mystique-1	Rev -2
Date:	Tuesday, November 12, 2013	Sheet 2 of	79





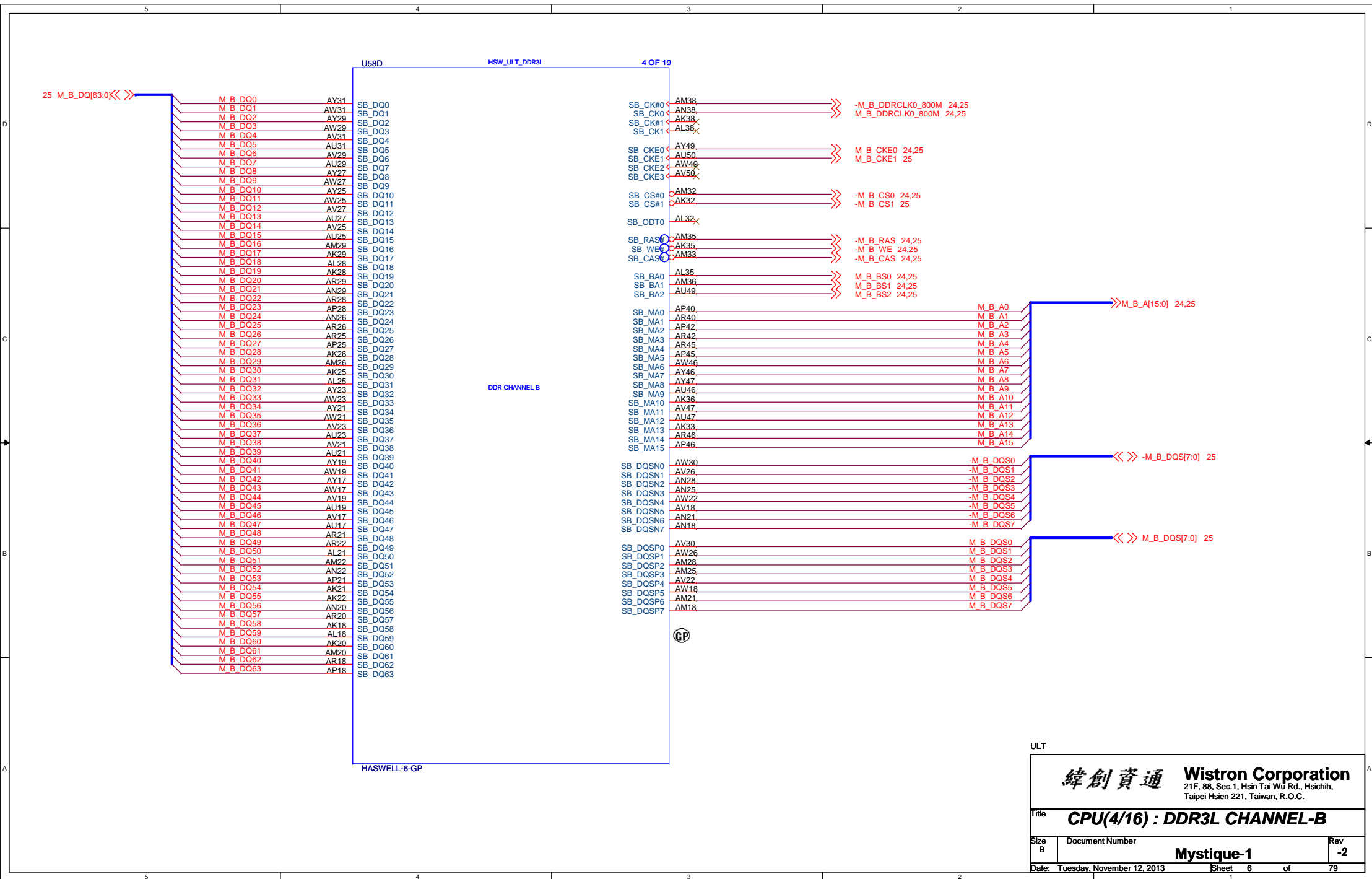
ULT		緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		CPU(2/16) : MISC/JTAG	
Size	Document Number	Mystique-1 Date: Tuesday, November 12, 2013 Sheet 4 of 79	
B		Rev	-2



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Title CPU(3/16) : DDR3L CHANNEL-A		
Size B	Document Number	Rev -2
Mystique-1		
Date: Tuesday, November 12, 2013	Sheet 5 of	79



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Title

CPU(4/16) : DDR3L CHANNEL-B

Size B

Document Number

Mystique-1

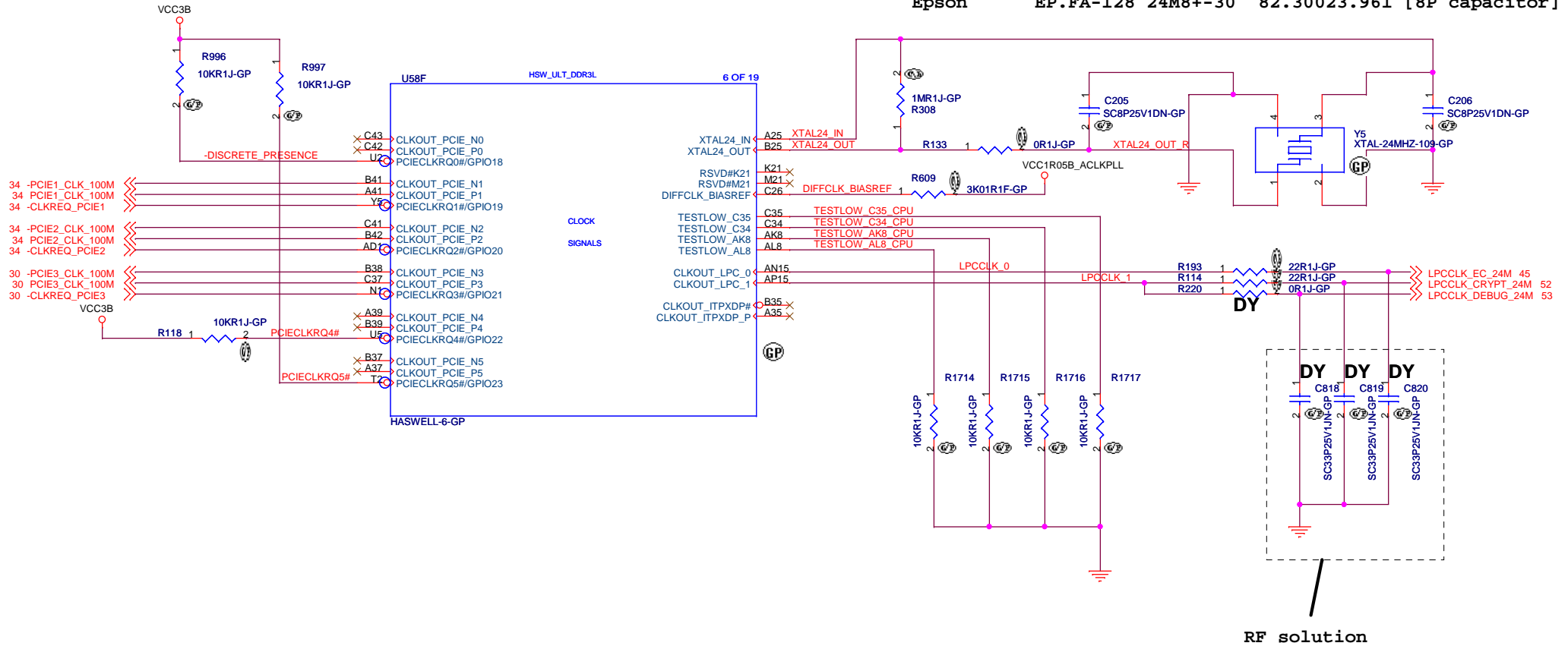
Rev -2

Date: Tuesday, November 12, 2013

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Y5
KDS 1ZZCAA24000CC0B 82.30023.991 [8P capacitor]
TXC 8Y24080002 82.30023.981 [8P capacitor]
Epson EP.FA-128 24M8+-30 82.30023.961 [8P capacitor]



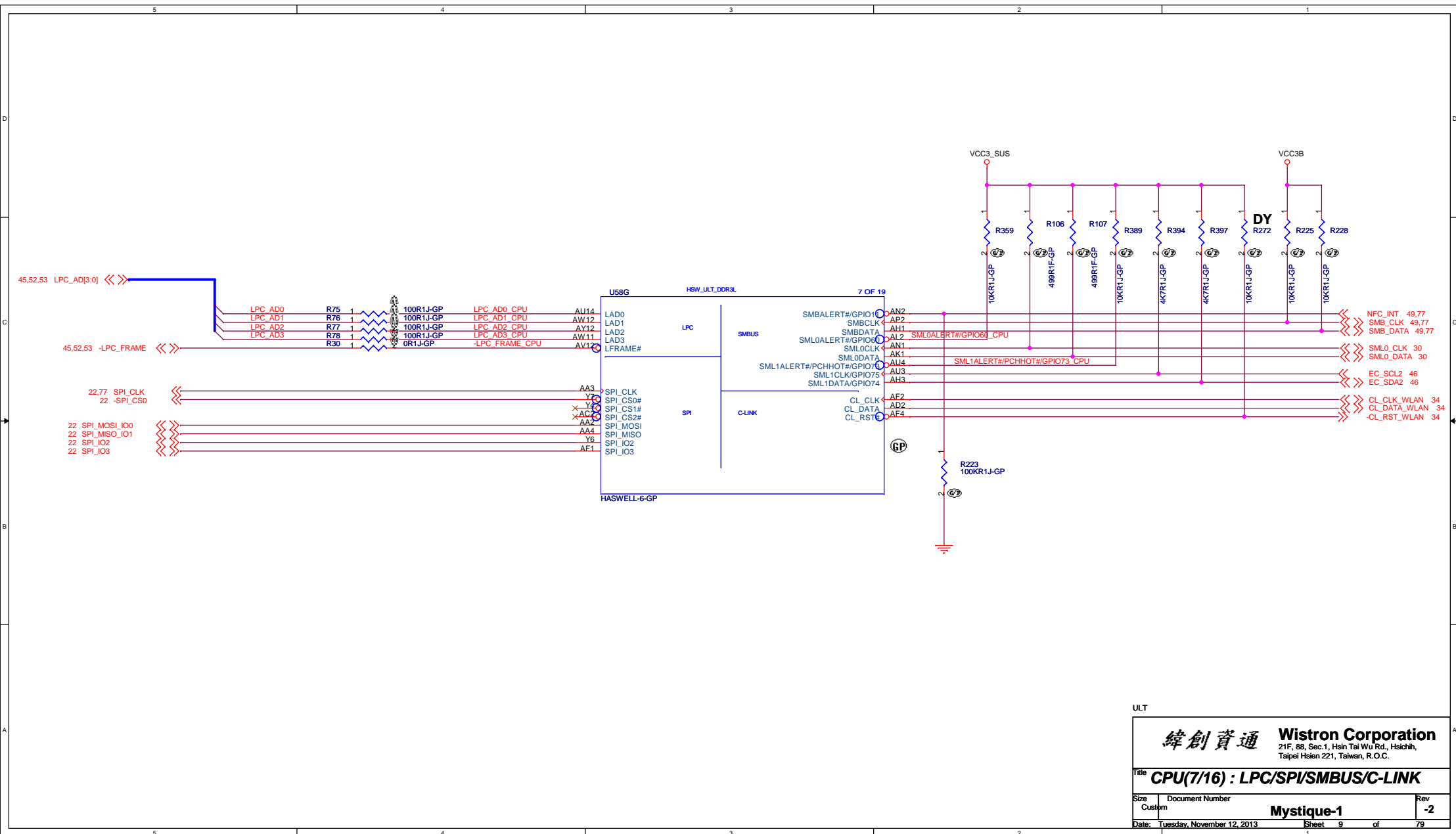
ULT

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Taipei Hsien 221, Taiwan, R.O.C.

Title **CPU(6/16) : CLOCK SIGNALS**

Size B Document Number **Mystique-1** Rev -2

Date: Tuesday, November 12, 2013 Sheet 8 of 79



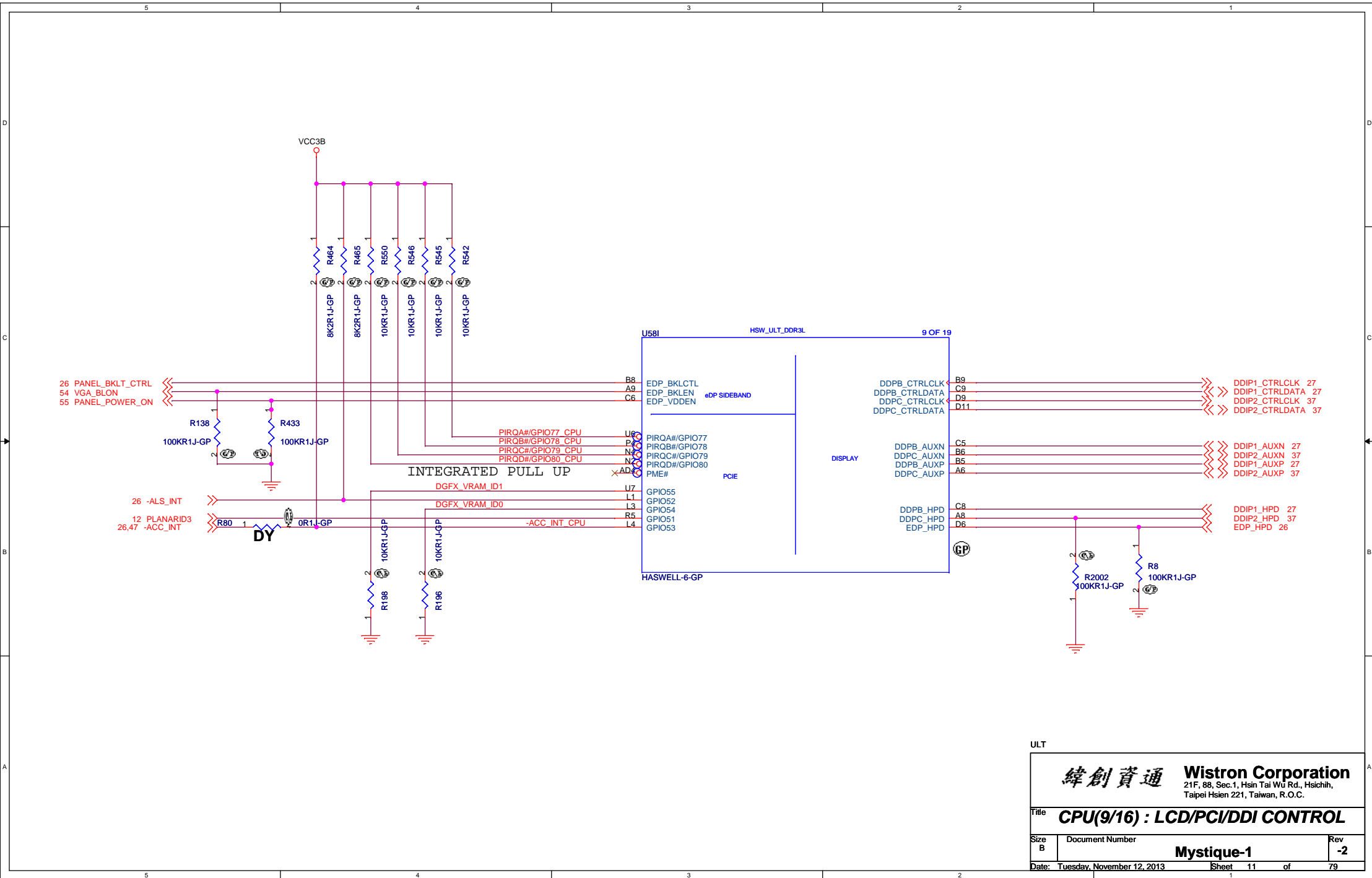
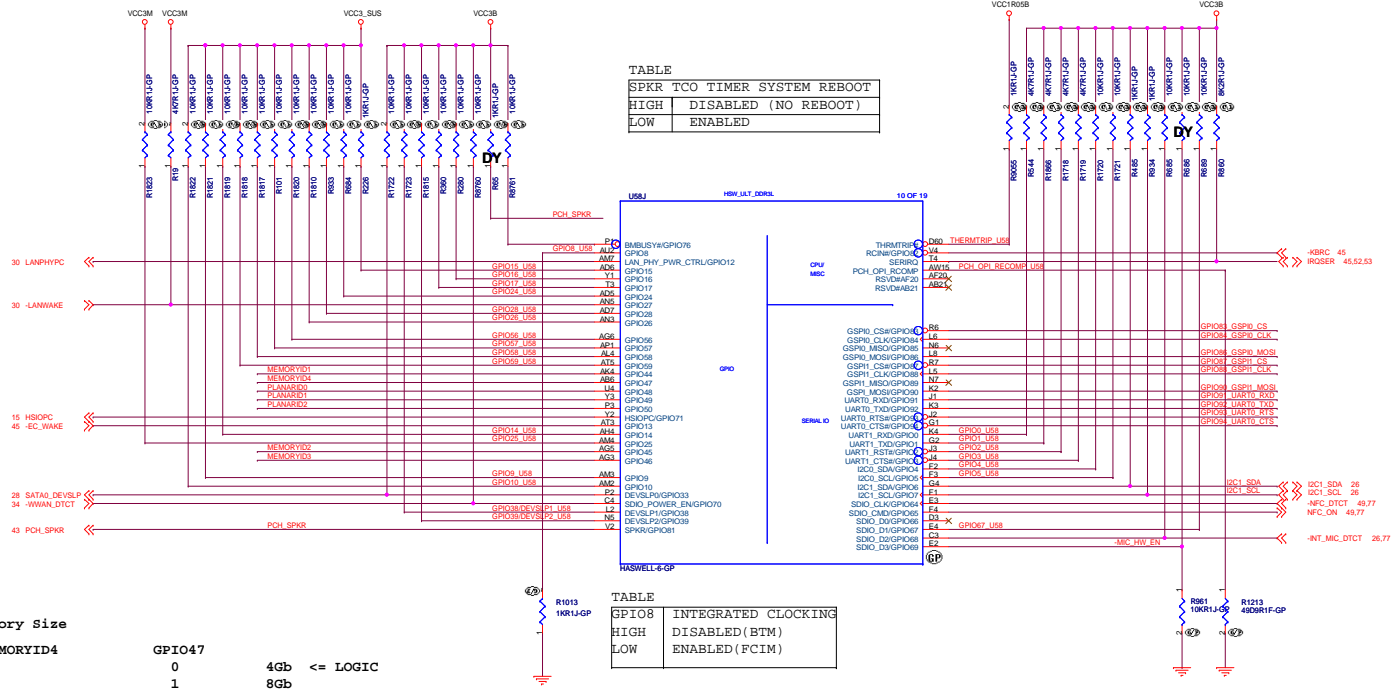
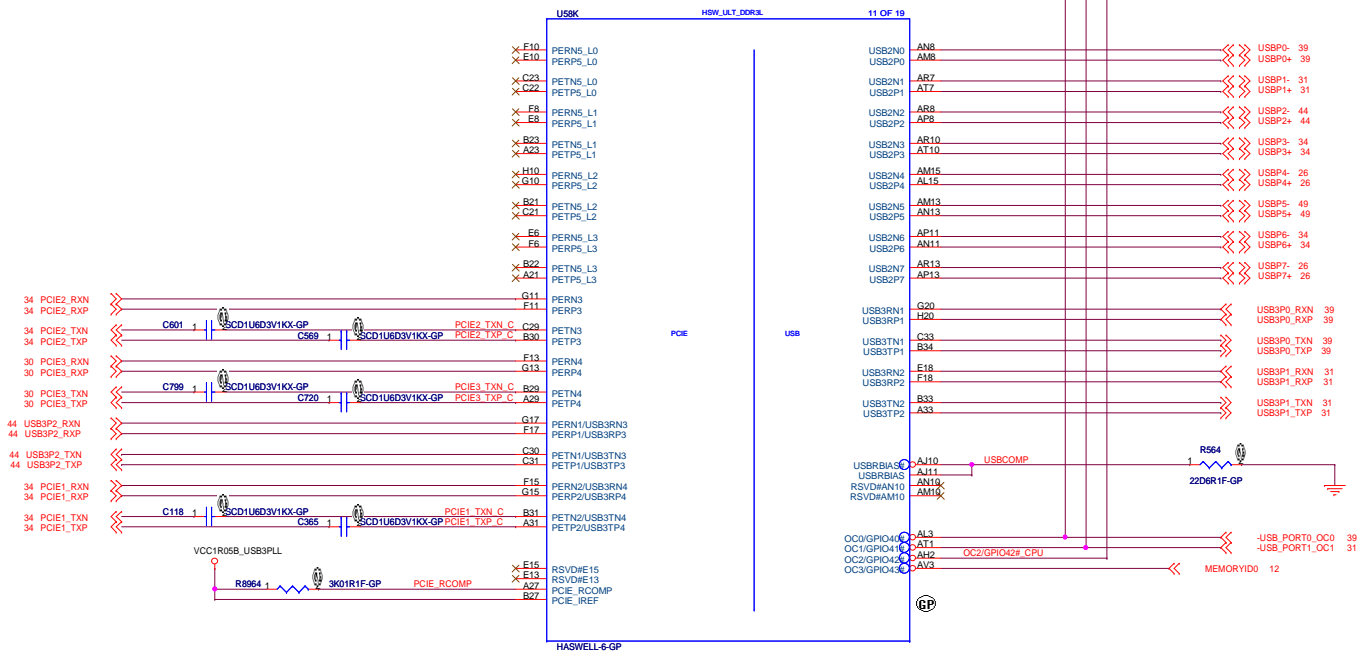


TABLE	
GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY



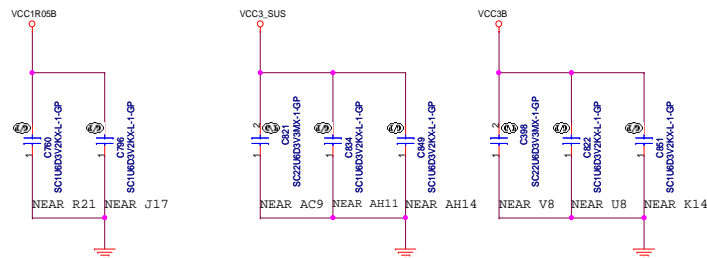
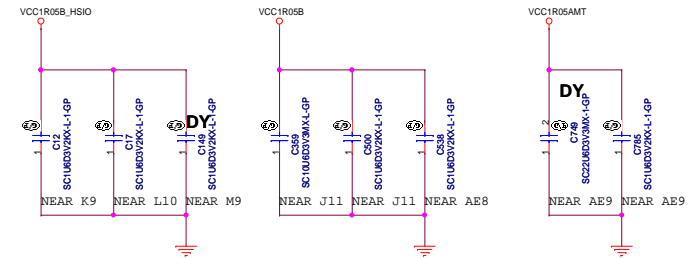
Flexible I/O Configuration			
I/O	High Speed Signals	Configuration	Net Name
Port 1	USB3 1	USB3 1	USB3P0
Port 2	USB3 2	USB3 2	USB3P1
Port 3	PCIE 1/USB3 3	USB3 3	USB3P2
Port 4	PCIE 2/USB3 4	PCIE 2	PCIE1
Port 5	PCIE 3	PCIE 3	PCIE2
Port 6	PCIE 4	PCIE 4	PCIE3
Port 7	PCIE 5 L0	PCIE 5 L0	PCIE4_0
Port 8	PCIE 5 L1	PCIE 5 L1	PCIE4_1
Port 9	PCIE 5 L2	PCIE 5 L2	PCIE4_2
Port 10	PCIE 5 L3	PCIE 5 L3	PCIE4_3
Port 11	PCIE 6 L0	PCIE 6 L0	PCIE5
Port 12	SATA 2/PCIE 6 L1	SATA 2	SATA 2
Port 13	SATA 1/PCIE 6 L2	SATA 1	SATA 1
Port 14	SATA 0/PCIE 6 L3	SATA 0	SATA 0

PCIe Port Assignment	
1	M.2 WLAN Slot Port 1
2	M.2 WLAN Slot Port 0
3	GbE PHY
4 (x4)	Reserved
5 (x1)	Reserved

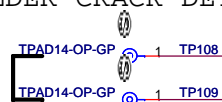


USB Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port (Debug)
2	USB 3.0 Docking
3	M.2 WWAN Slot
4	Touch Panel
5	Fingerprint Reader
6	M.2 WLAN Slot
7	USB Camera

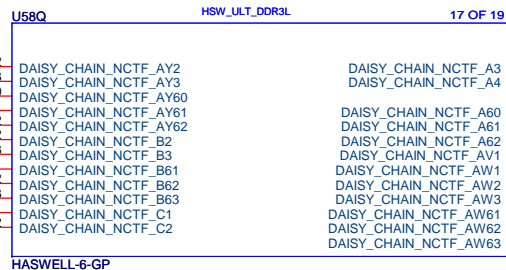
USB 3.0 Port Assignment	
0	USB 3.0 System Port (AOU)
1	USB 3.0 System Port (Debug)
2	USB 3.0 Docking



FOR SOLDER CRACK DETECTION



DC TEST AY2 AW2	AY2
DC TEST AY3 AW3	AY3
TP DC TEST AY60	AY60
DC TEST AY61 AW61	AY61
DC TEST AY62 AW62	AY62
TP DC TEST B2	B2
DC TEST A3 B3	B3
DC TEST A61 B61	B61
DC TEST B62 B63	B62
DC TEST C1 C2	C1
	C2



FOR SOLDER CRACK DETECTION

A3	DC TEST A3 B3	TP110	1	TPAD14-OP-GP
A4	TP DC TEST A4			
A60	TP DC TEST A60	TP111	1	TPAD14-OP-GP
A61	DC TEST A61 B61			
A62	TP DC TEST A62	TP112	1	TPAD14-OP-GP
AW1	TP DC TEST AW1	TP113	1	TPAD14-OP-GP
AW2	TP DC TEST AW1	TP114	1	TPAD14-OP-GP
AW2	DC TEST AY2 AW2			
AW3	DC TEST AY3 AW3			
AW61	DC TEST AY61 AW61			
AW62	DC TEST AY62 AW62			
AW63	TP DC TEST AW63	TP115	1	TPAD14-OP-GP

ULT

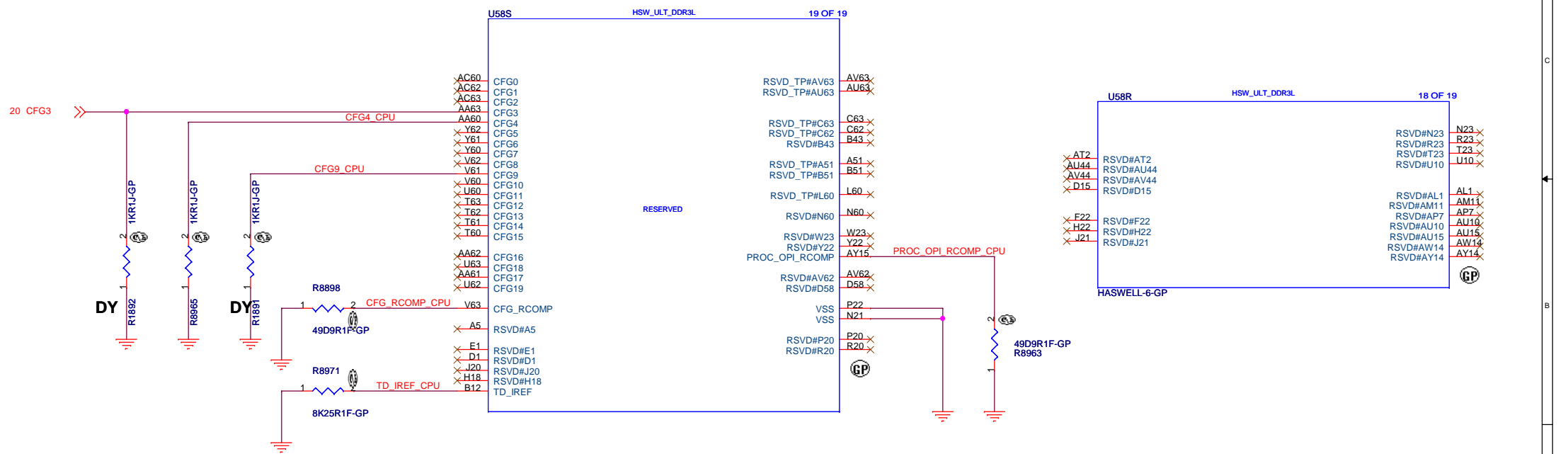
緯創資通 Wistron Corporation
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Title CPU(15/16) : NCTF

Size B Document Number Mystique-1 Rev -2

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TABLE	
CFG4 : Display Port Presence	
1	: Disabled
0	: Enabled
CFG9 : SVID Bus Communication	
1	: Enabled
0	: Disabled



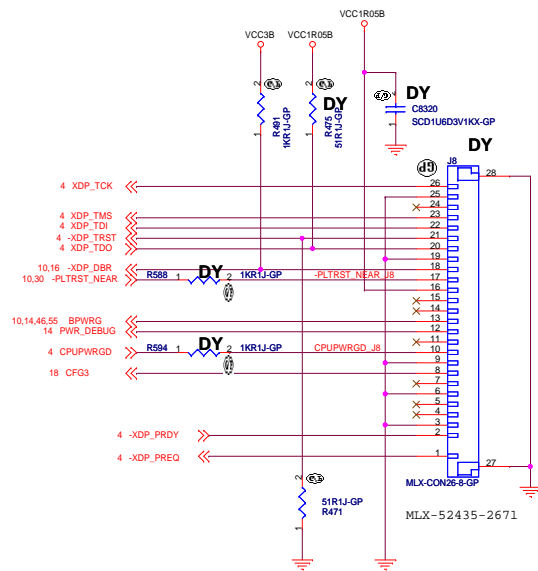
ULT	
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title CPU(16/16) : CFG/RESERVE	
Size B	Document Number
Mystique-1	
Date: Tuesday, November 12, 2013	Sheet 18 of 79
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<div>Date:</div> <div>Tuesday, November 12, 2013</div>	<div>Sheet</div> <div>19</div>	<div>of</div> <div>79</div>	

JTAG

[illegible]

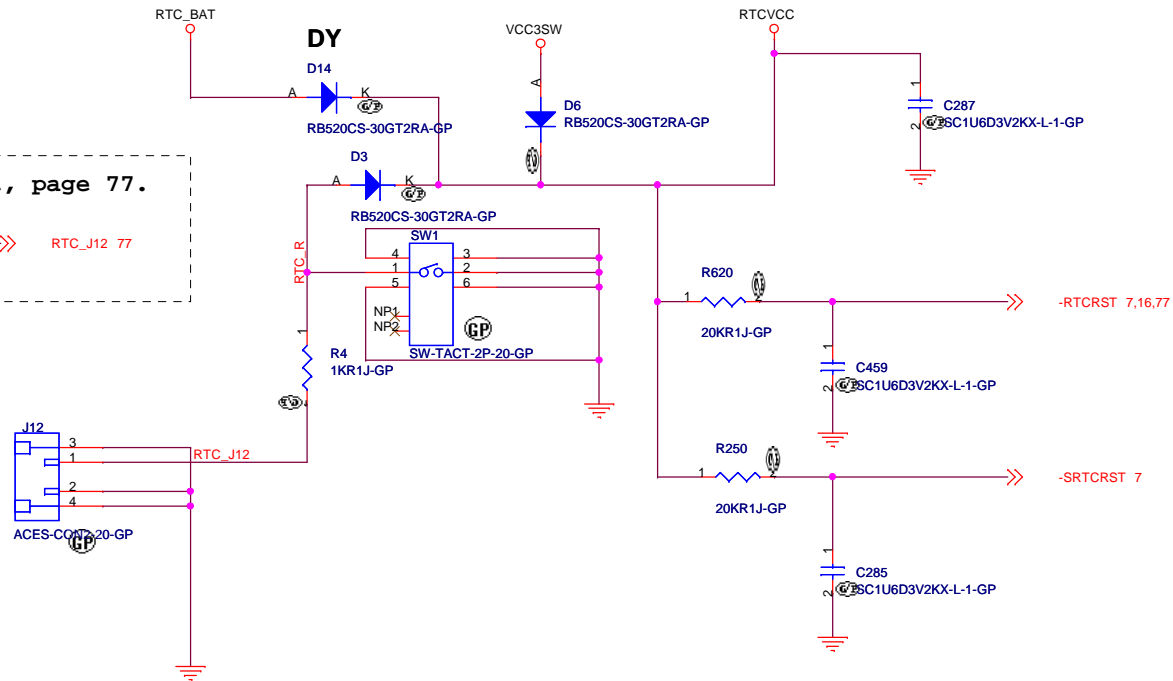
SIGNAL	REF DES	ENABLE	DISABLE
TDO	R475	ASM	NO ASM
TRST#	R471	ASM	ASM
DBRST#	R491	ASM	ASM
RESET#	R588	ASM	NO ASM
PWRGD	R594	ASM	NO ASM
	J8	ASM	NO ASM
	C8320	ASM	NO ASM

SIGNAL	REF DES	ENABLE	DISABLE
TDO	R509	220	NO ASM
	R943	100	NO ASM
TMS	R530	220	NO ASM
	R946	100	NO ASM
TDI	R515	220	NO ASM
	R945	100	NO ASM
TCK	R541	51	51
CPUCORE_PWRGD	R511	ASM	NO ASM
-RSMRST	R514	ASM	NO ASM
	J9	ASM	NO ASM

LOGIC

for test point, page 77.

RTC_J12 >>> RTC_J12 77



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Title RTC BATTERY

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SPI	vPRO	non-vPRO
U49	16MB	8MB

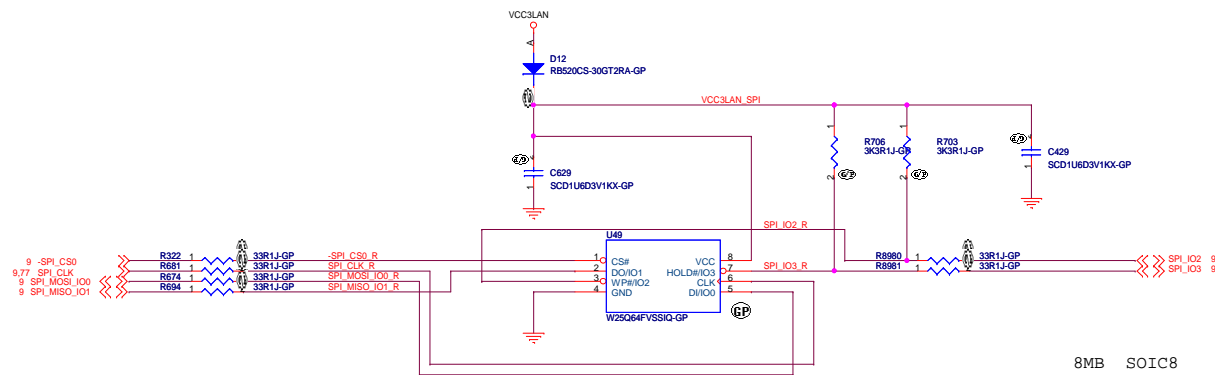
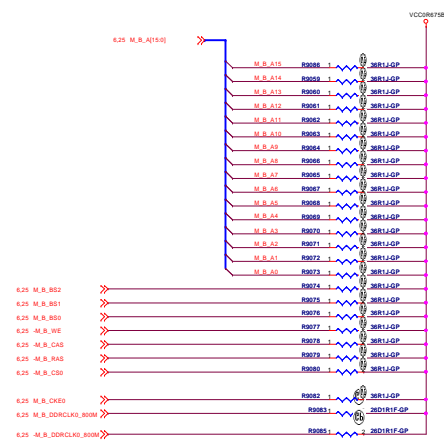
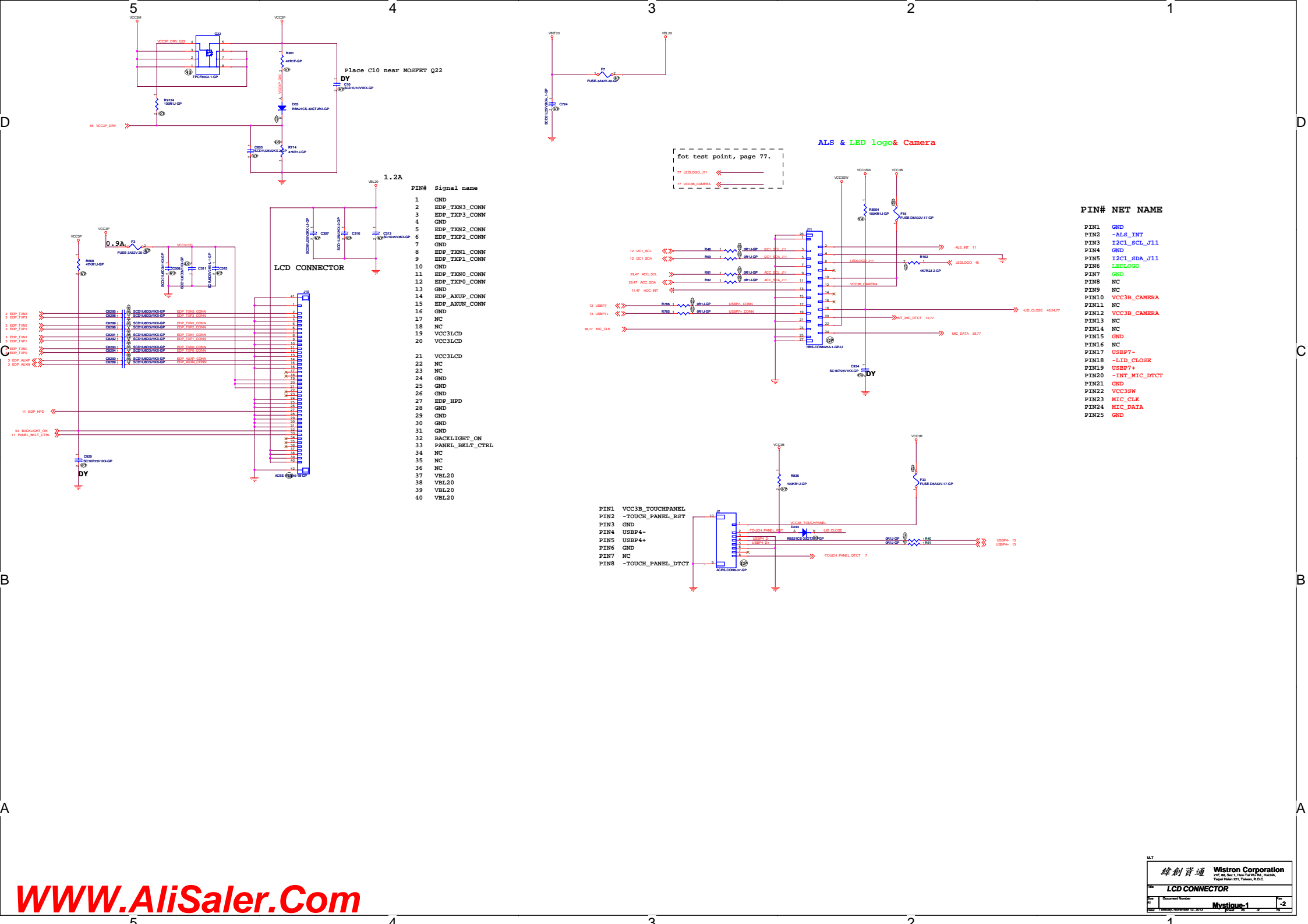


TABLE					
SF100	PIN	HEADER	INTERFACE	(TOP VIEW)	
1	VCC	D12.1	GND	GND	2
3	CS#	R322.2	R681.2	CLK	4
5	MISO	R694.2	R674.2	MOSI	6
7 (KEY)	N/A		N/A	(RESET)	8

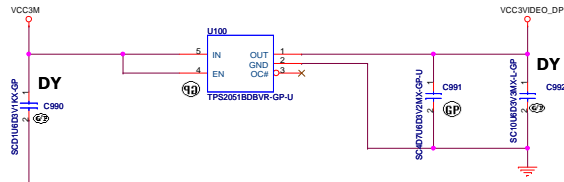
8MB	SOIC8		
WINBOND	W25Q64FVSSIQ	72.25Q64.K01	
MACRONIX	MX25L6475EM2I-10G	72.25647.001	
MICRON	N25Q064A13ESEC0F	72.25Q64.G01	
16MB	SOIC8		
WINBOND	W25Q128FVSIQ	72.25128.0E1	
MACRONIX	MX25L12875FM2I-10G	72.12873.001	
MICRON	N25Q128A13ESEC0F	72.25128.0B1	





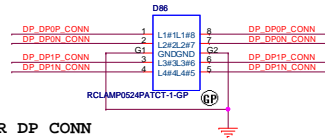
PLACE NEAR DP CONN

Typ. 500mA
Max 1.5A

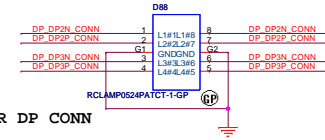


Pin#1 condition	Connection	Note
LOW	nA to nB1	w/ Dongle
HIGH	nA to nB2	w/o Dongle

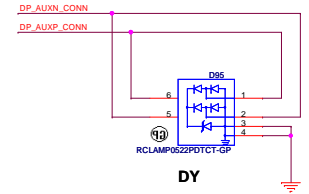
NEAR DP CONN



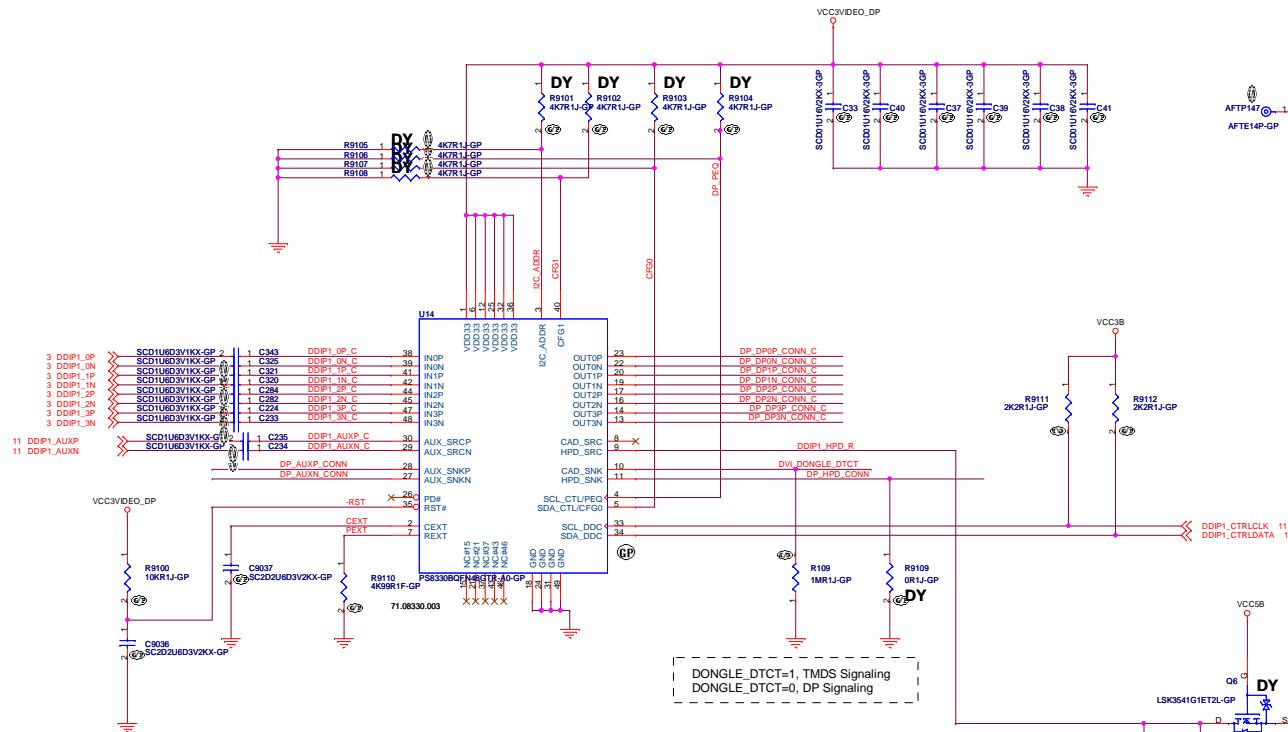
NEAR DP CONN



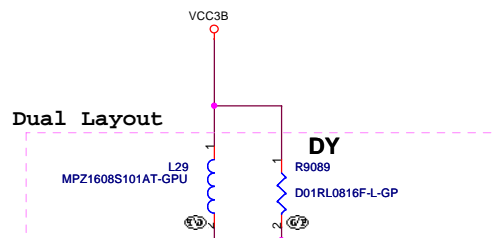
FOR SYSTEM DP NEAR DP CONN



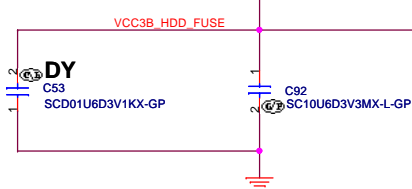
Add Demux for DP in Cable Dock Case



DONGLE_DTCT=1, TMDS Signaling
DONGLE_DTCT=0, DP Signaling



M.2 SSD Type-M 2280

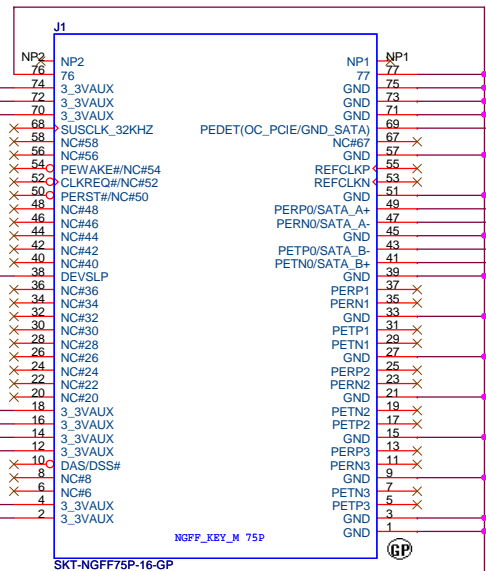


12 SATA_DEVSLP

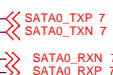
R100 1

0R1J-GP

SATA0_DEVSLP J1



-HDD_DTCT 47



ULT

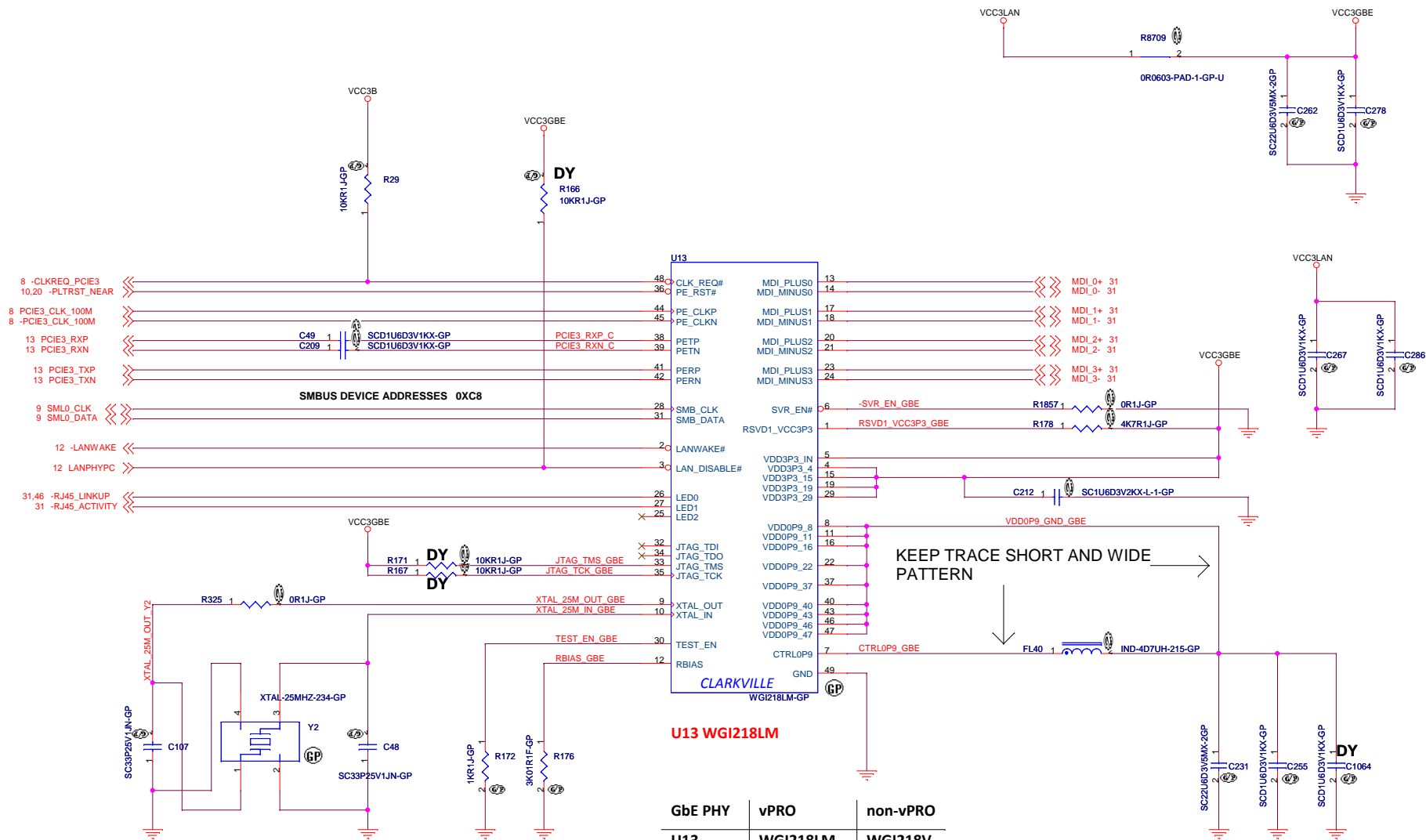
緯創資通 Wistron Corporation
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Title **SATA HDD CONN**

Size A3 Document Number

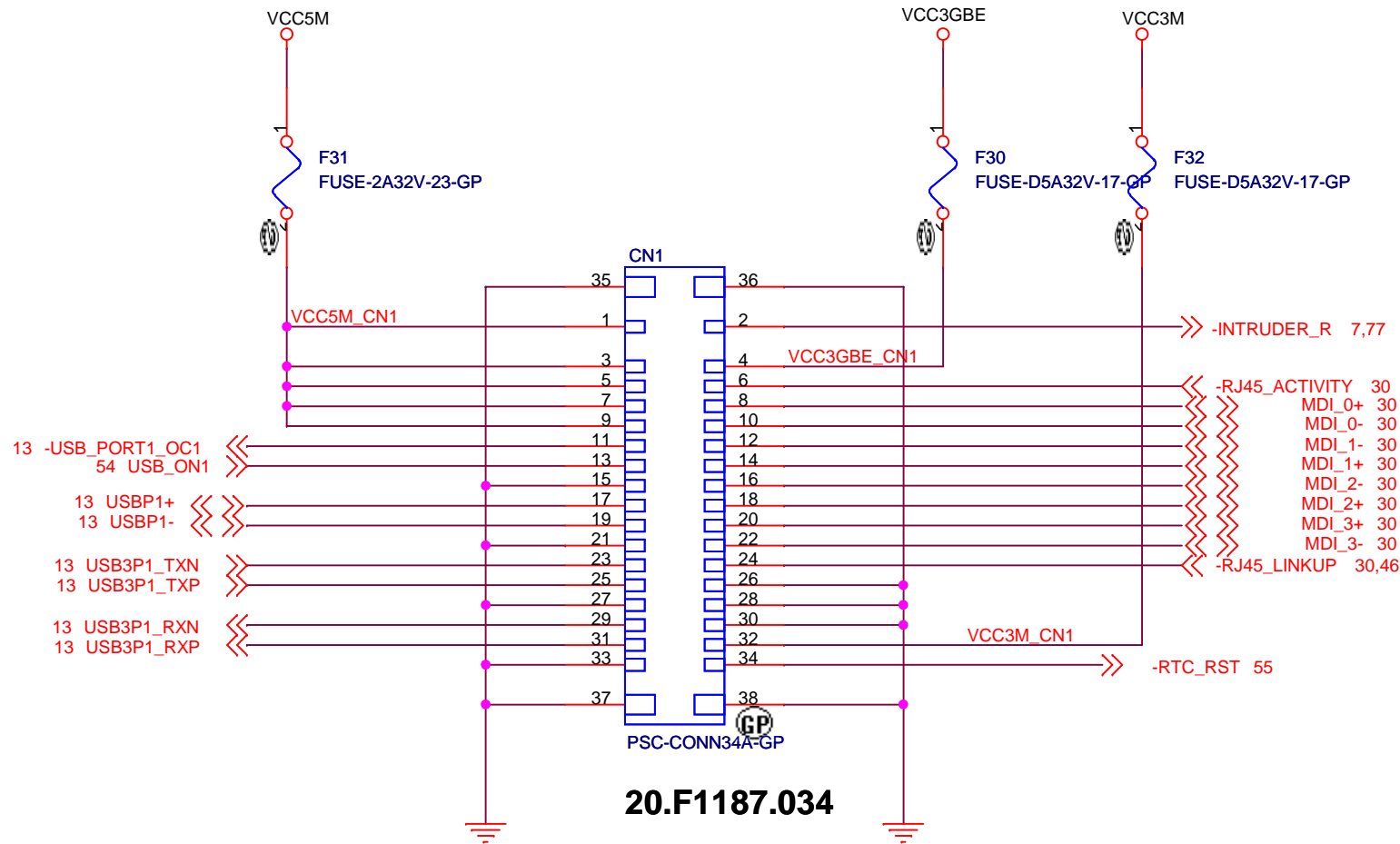
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Rev -2



Y2
 KDS 1ZZCAA25000CC0C 82.30020.N91 [12P capacitor]
 TXC 8Y25000004 82.20026.191 [33P capacitor]
 Epson EP.FA-128 25M20+-30 82.30020.P51 [33P capacitor]

GbE PHY	vPRO	non-vPRO
U13	WGI218LM	WGI218V



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Title **GBE / USB3.0 BTB CONNECTOR**

Size A Document Number

Mystique-1

Rev **-2**

Date: Tuesday, November 12, 2013

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Title			
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Size	Document Number		Rev
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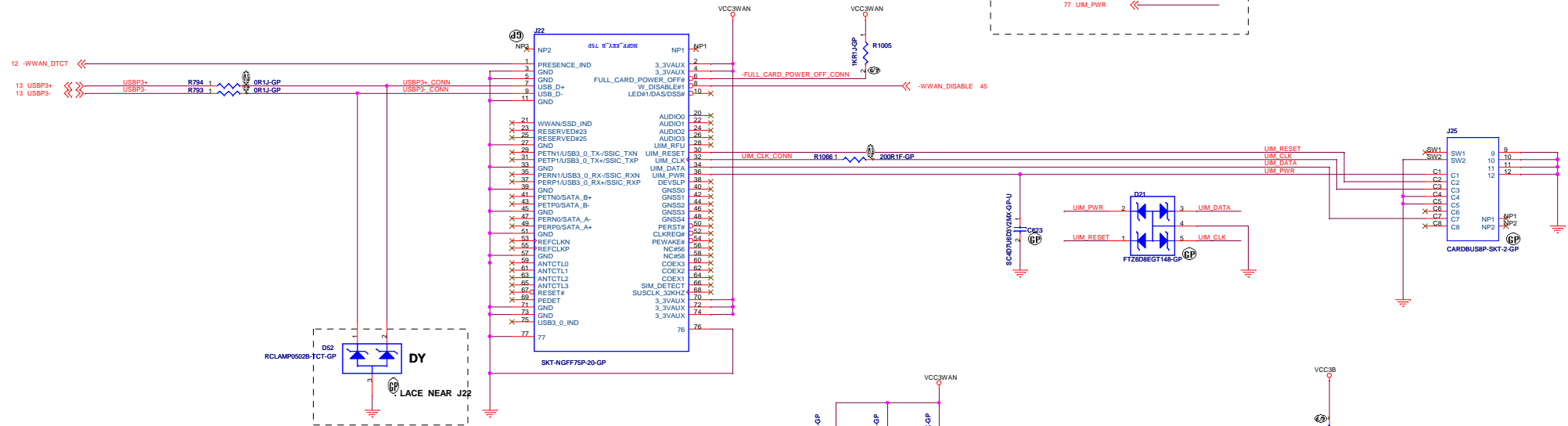
ULT

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Title BLANK			
Size A4	Document Number Mystique-1		Rev -2
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TYPE-B M.2 CARD FOR WWAN/SSD

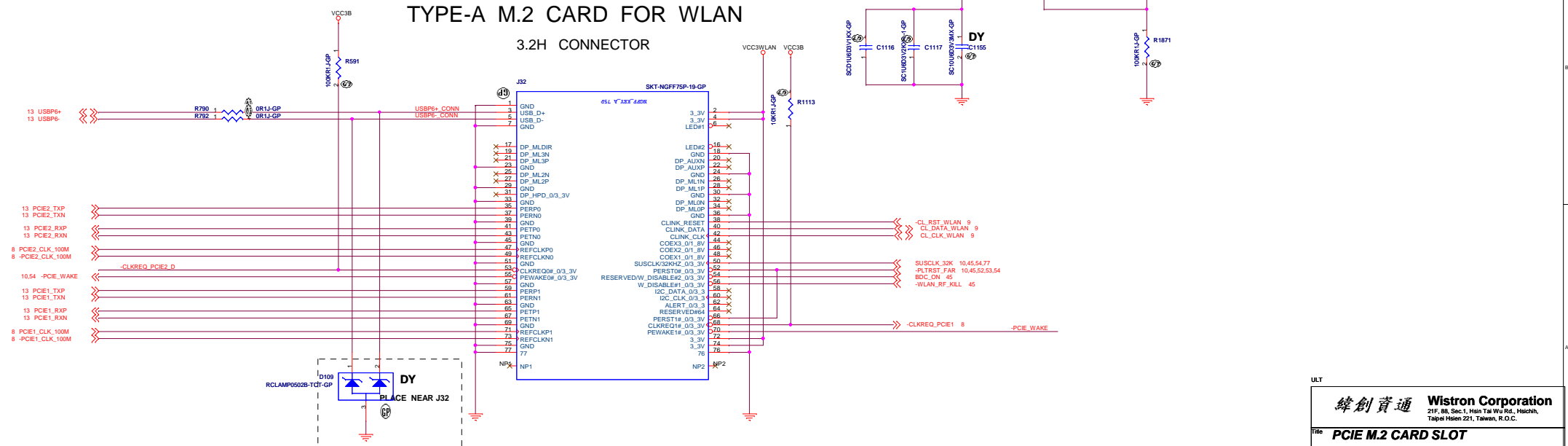
3.2H CONNECTOR

for test point, page 77.



TYPE-A M.2 CARD FOR WLAN

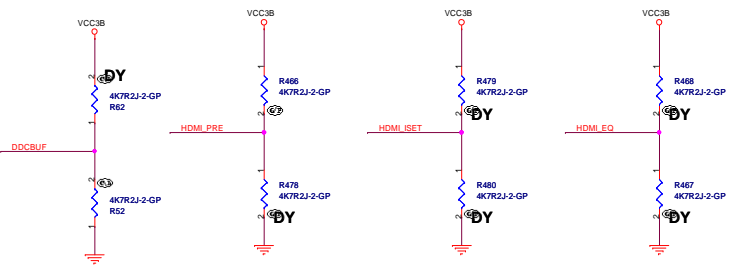
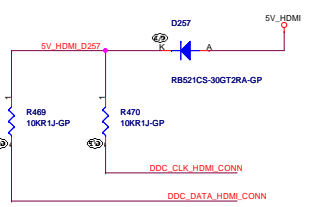
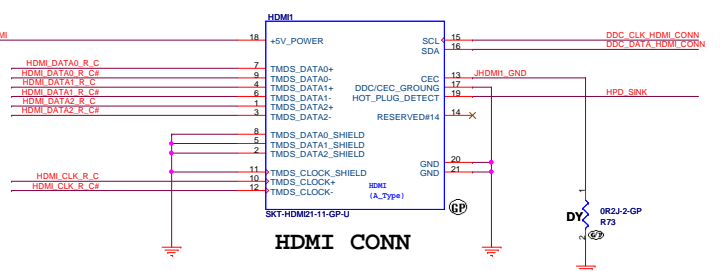
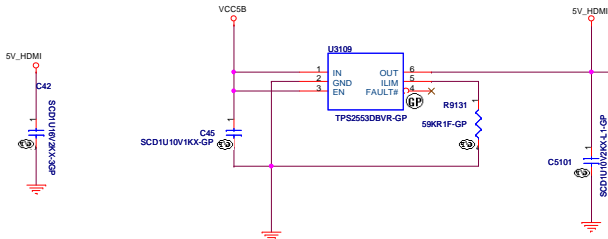
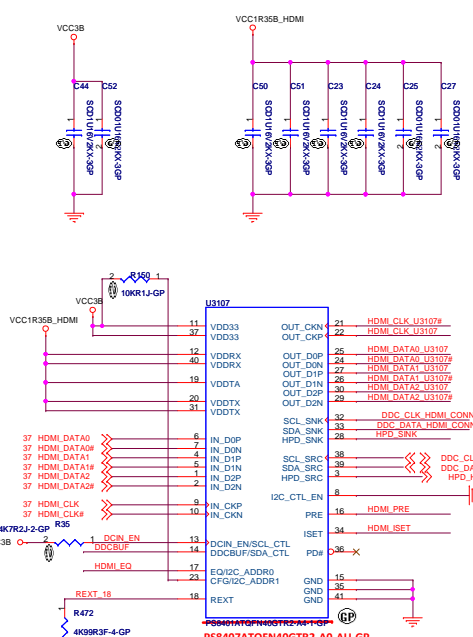
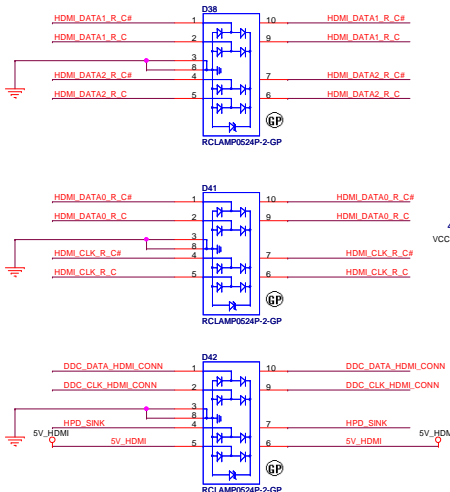
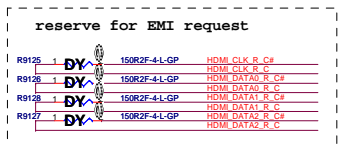
3.2H CONNECTOR



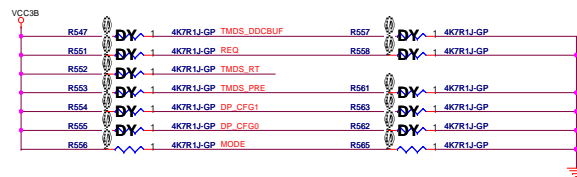
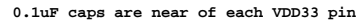
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Size A4	Document Number Mystique-1	Rev -2
Date: Tuesday, November 12, 2013		Sheet 35 of 79



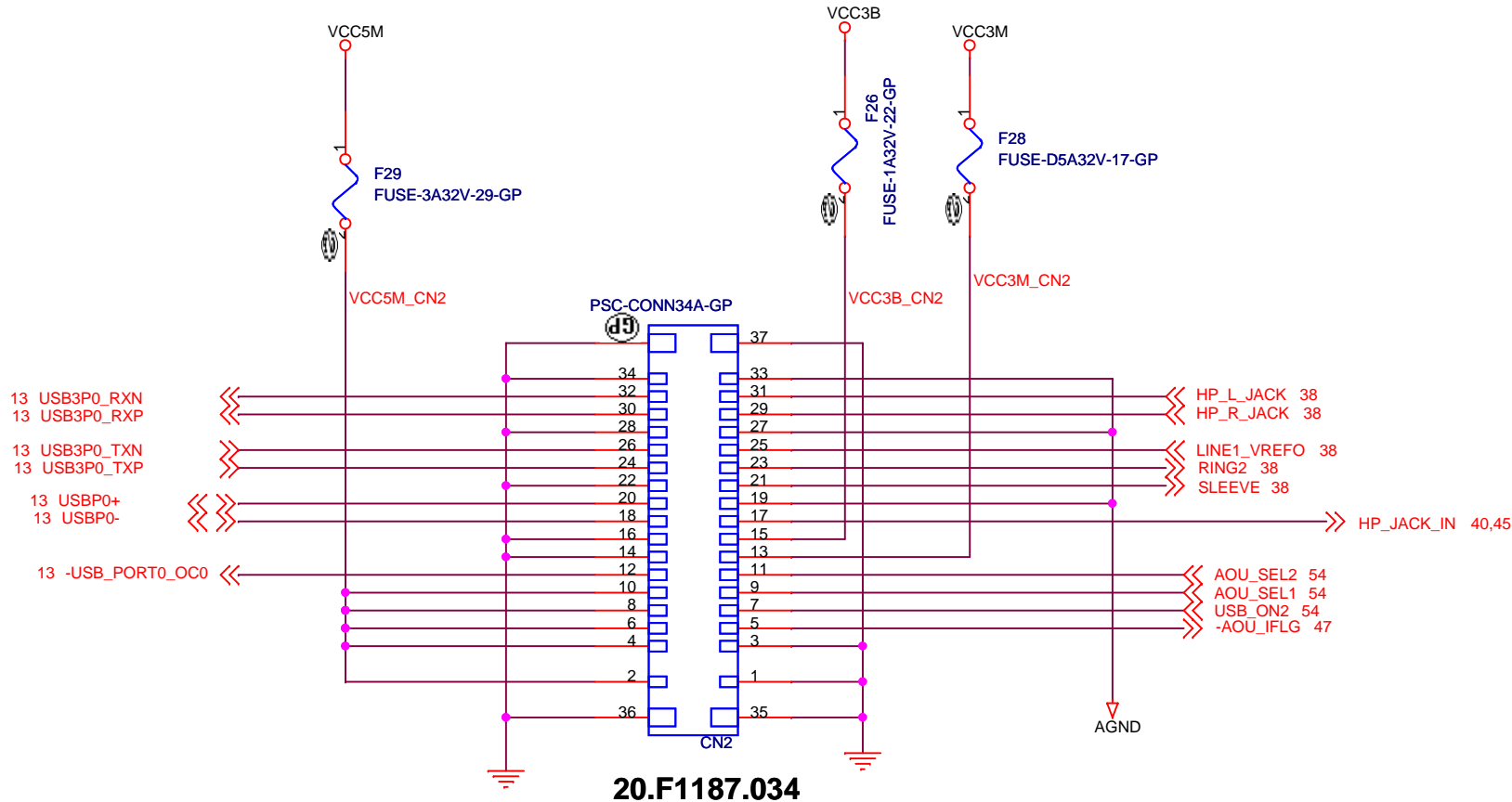
H: HDMI is higher priority than DOCKDP
L: DOCKDP is higher priority than HDMI



For Automatic Switching:
SW1 = L: DP output has higher priority.
The pin Internal pull down at ~150K, 3.3V I/O
SW1 = H: TMDS output has higher priority.

↑
LOGIC





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Title **AUDIO /USB3.0 BTB CONNECTOR**

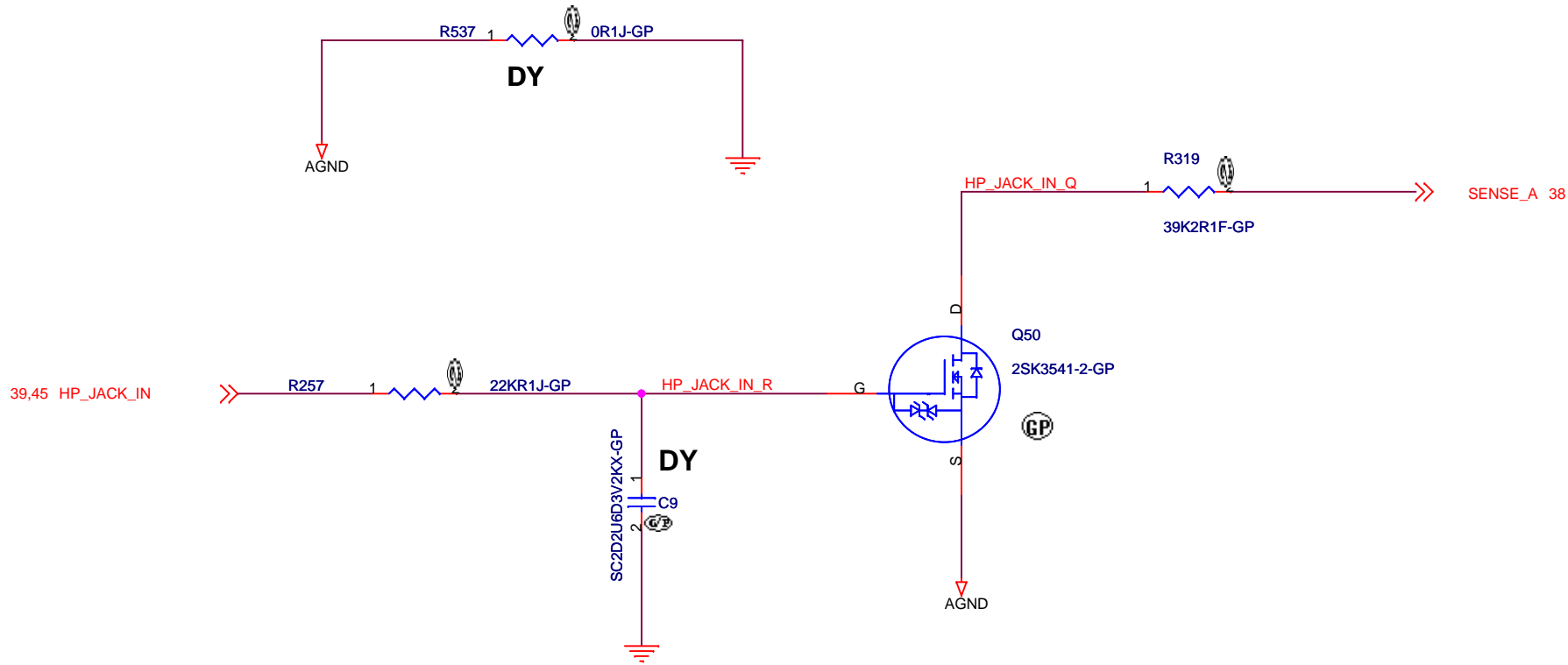
Size A4 Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

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Title **AUDIO JACK SENSE**

Size A4 Document Number

Mystique-1

Rev **-2**

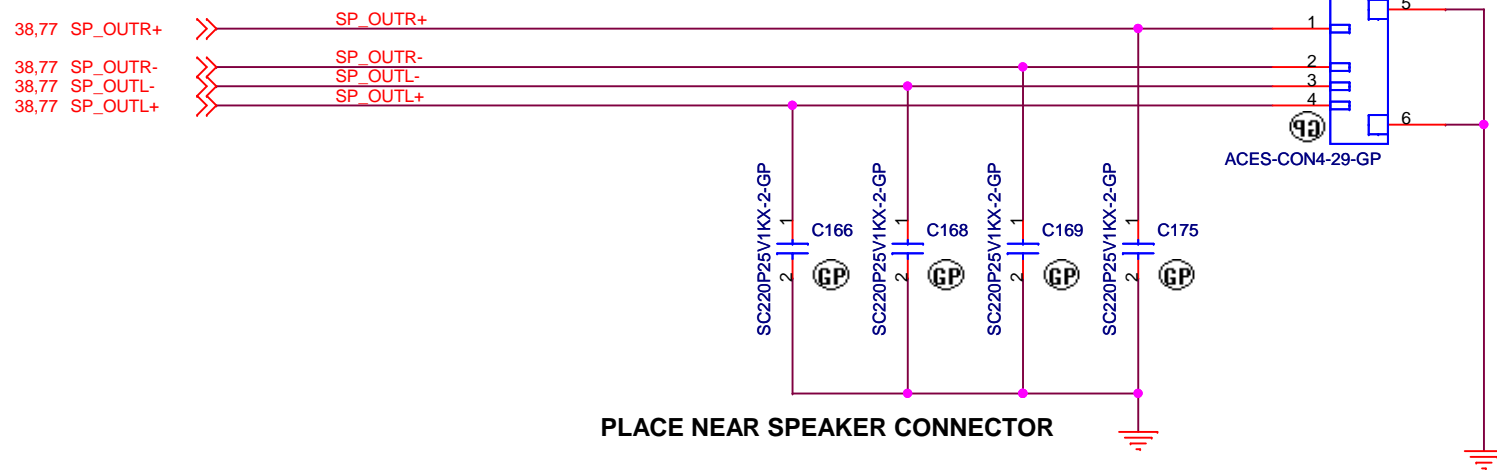
Date: Tuesday, November 12, 2013

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<div>緯創資通</div>		<div>Wistron Corporation</div>	
<div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>			
<div>Title</div> <div>BLANK</div>			
<div>Size</div> <div>B</div>	<div>Document Number</div> <div>Mystique-1</div>		<div>Rev</div> <div>-2</div>
<div>Date:</div> <div>Tuesday, November 12, 2013</div>		<div>Sheet</div> <div>41</div>	<div>of</div> <div>79</div>



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緯創資通

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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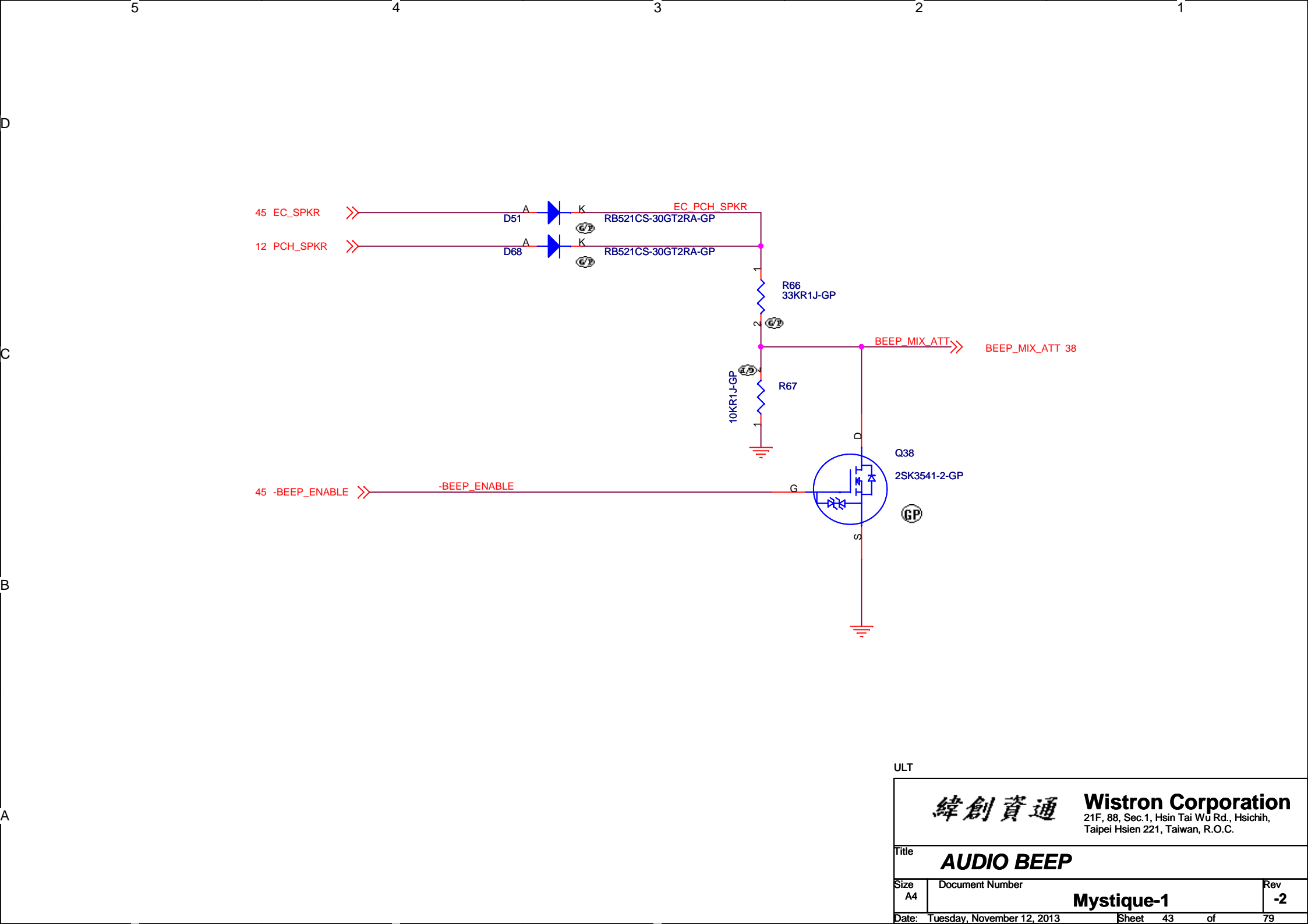
Title **AUDIO SPEAKER**

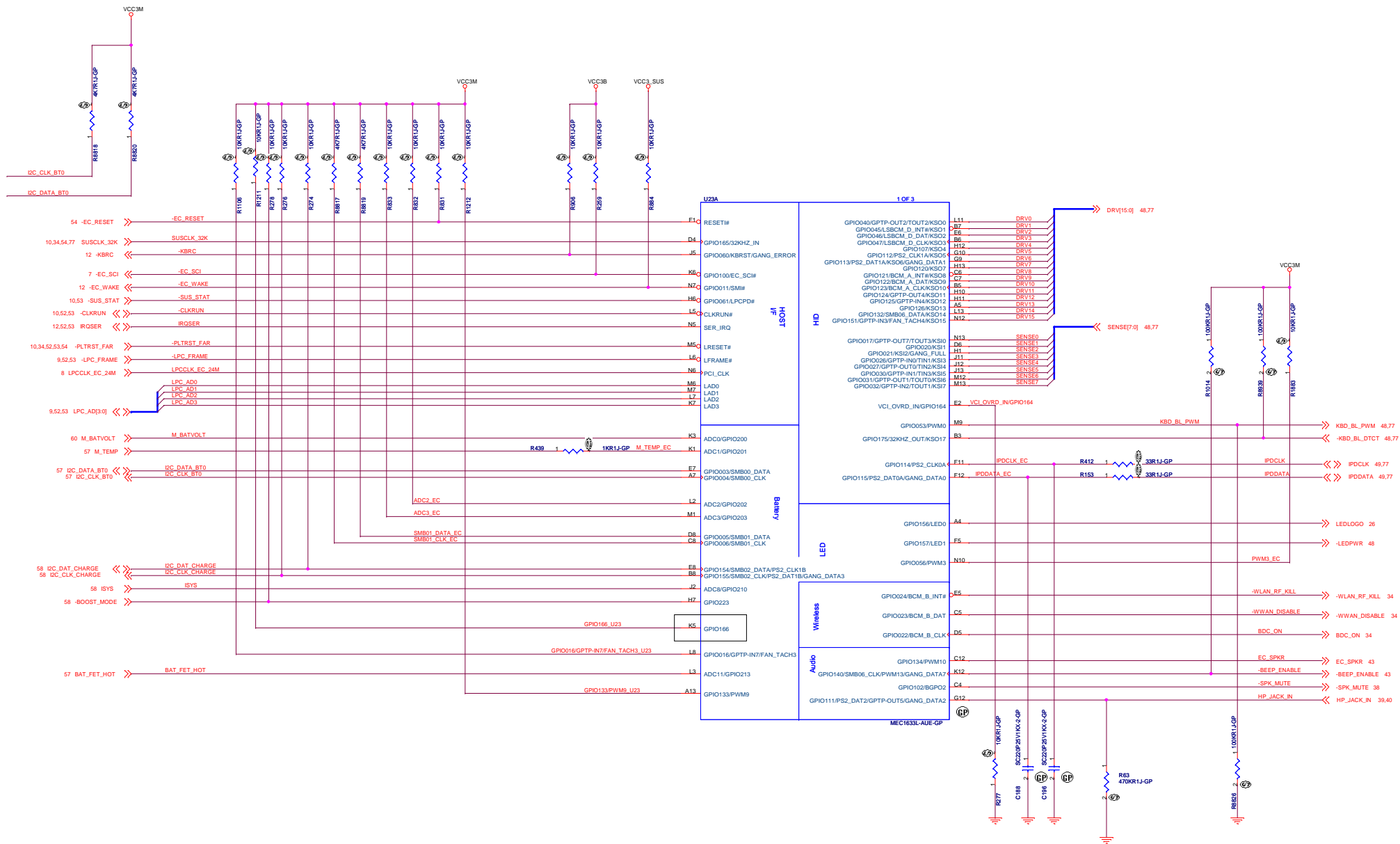
Size A4 Document Number

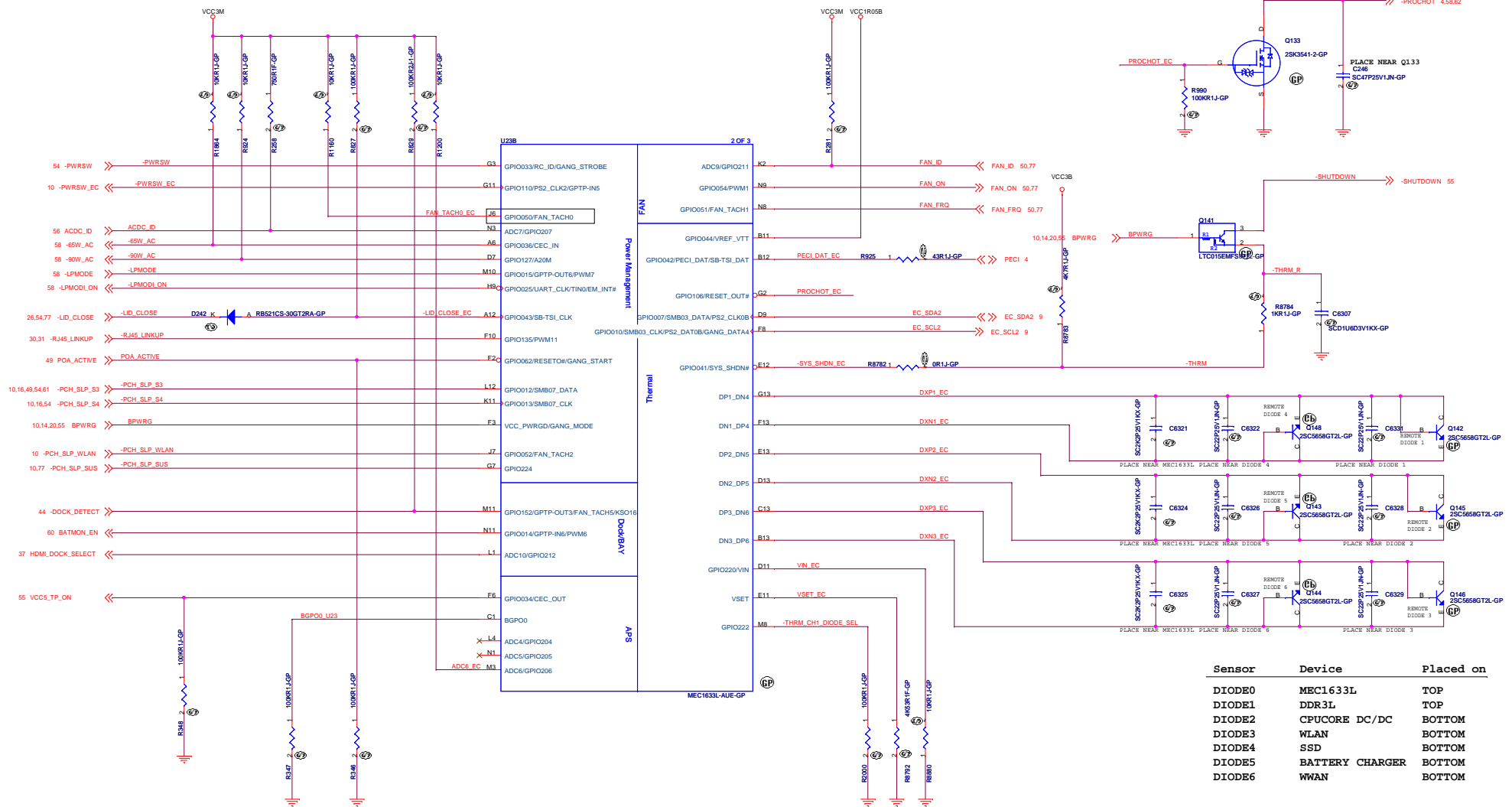
Mystique-1 Rev **-2**

Date: Tuesday, November 12, 2013

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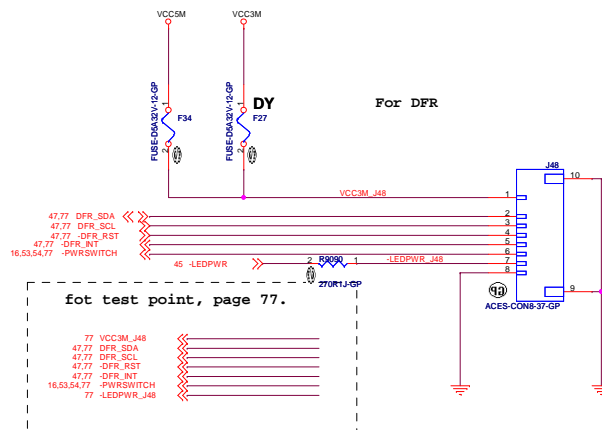
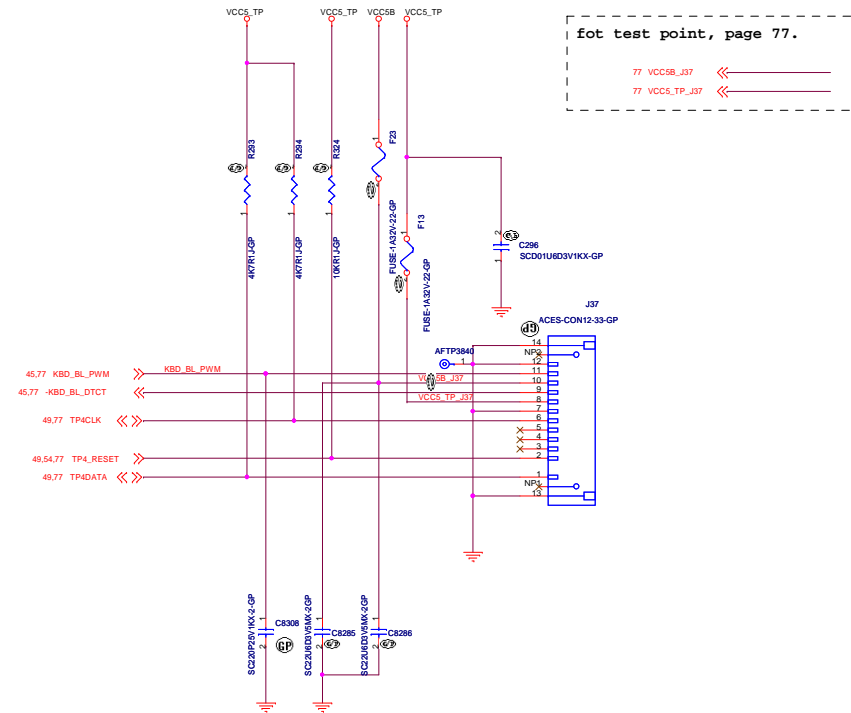
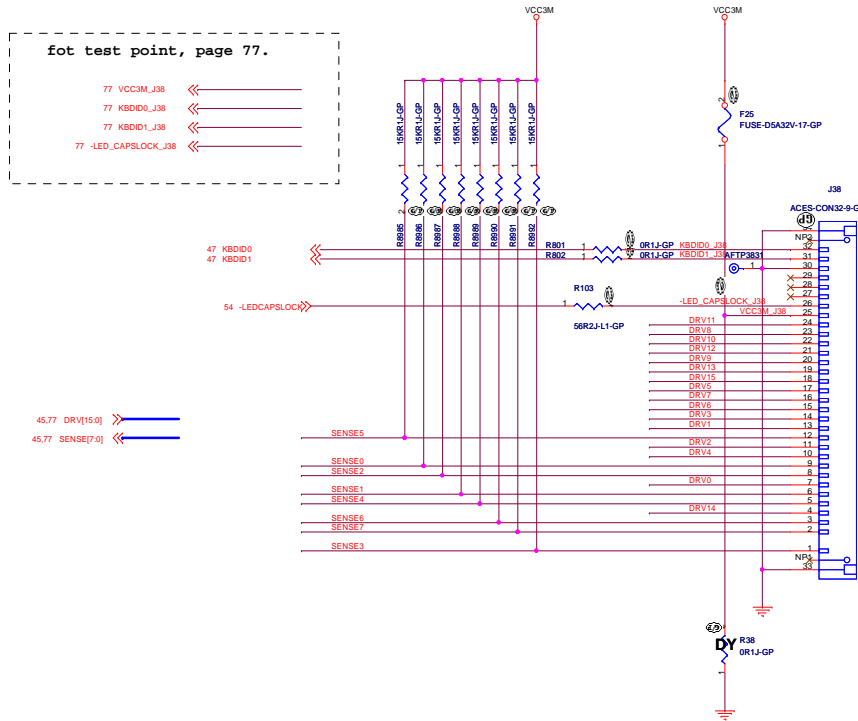


Sensor	Device	Placed on
DIODE0	MEC1633L	TOP
DIODE1	DDR3L	TOP
DIODE2	CPUCORE DC/DC	BOTTOM
DIODE3	WLAN	BOTTOM
DIODE4	SSD	BOTTOM
DIODE5	BATTERY CHARGER	BOTTOM
DIODE6	WWAN	BOTTOM



KBD (32PIN)

TRACKPOINT (12PIN)



<Pin Assignment of DFR connector>

- 1 VCC3M OR VCC5M
- 2 DFR_SDA
- 3 DFR_SCL
- 4 -DPR_RST
- 5 -DPR_INT
- 6 -PWRSWITCH
- 7 -LED_PWR
- 8 GND

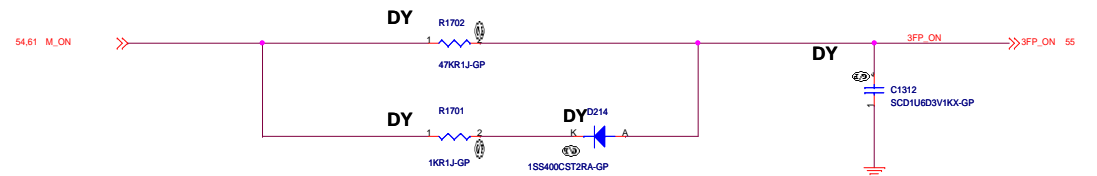
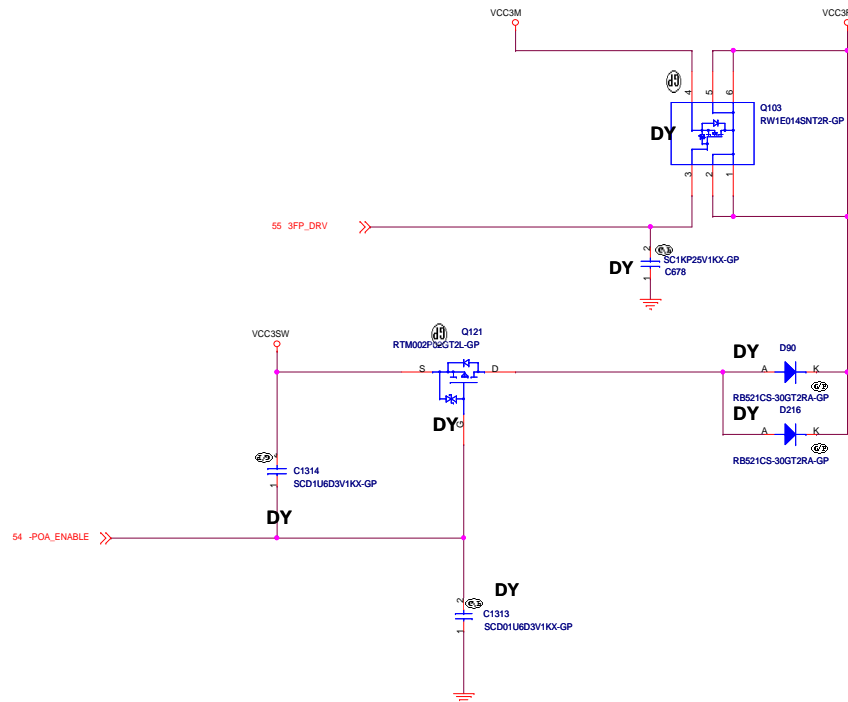
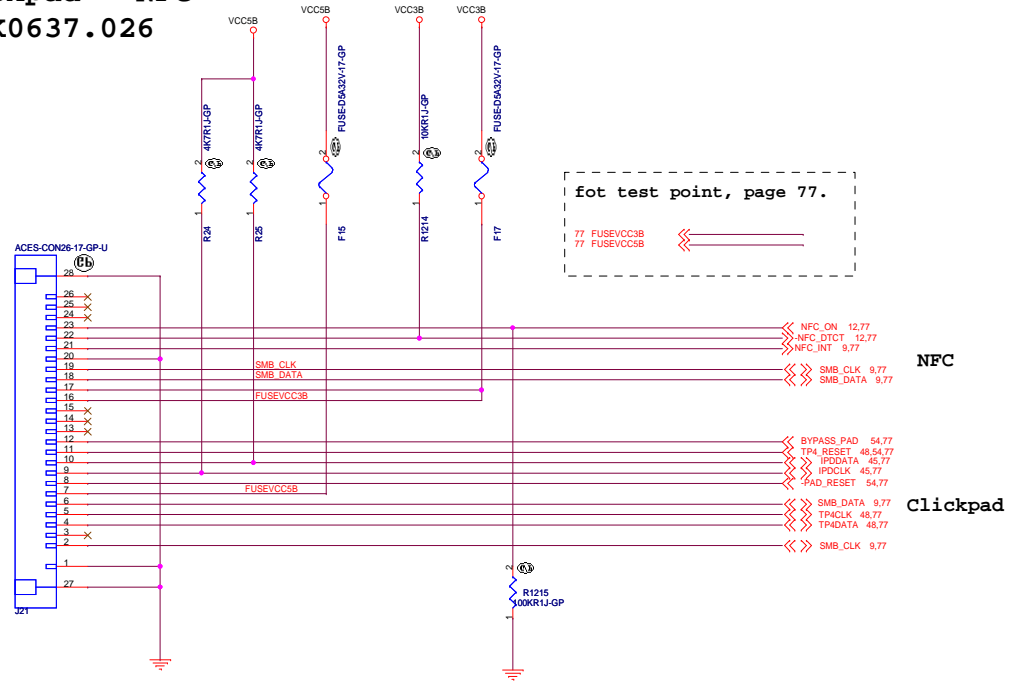
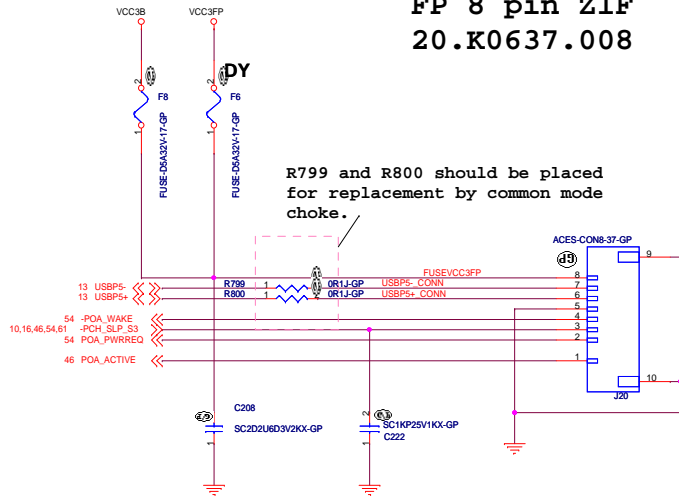
ULT

緯創資通 Wistron Corporation
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KEYBOARD CONNECTOR		
Size	Document Number	Rev
A2	Mystique-1	-2
Date	Tuesday, November 12, 2013	Sheet 49 of 73

Clickpad + NFC 20.K0637.026

FP 8 pin ZIF
20.K0637.008



for test point, page 77.

77 VCC5B_F4



VCC5B

FAN CURRENT IS 0.5A MAX
FUSE 2.0A



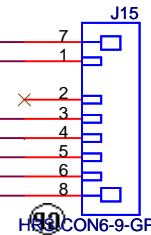
F4
FUSE-2A32V-23-GP

VCC5B_F4

46,77 FAN_ON

FAN_ON
AFTP3847

1



PIN# NET NAME

PIN1 VCC5B_F4
PIN2 NC
PIN3 FAN_ON
PIN4 GND
PIN5 FAN_FRQ
PIN6 FAN_ID

FAN_ID 46,77

FAN_FRQ 46,77

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Title

FAN CONNECTOR

Size
A4

Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

Sheet 50

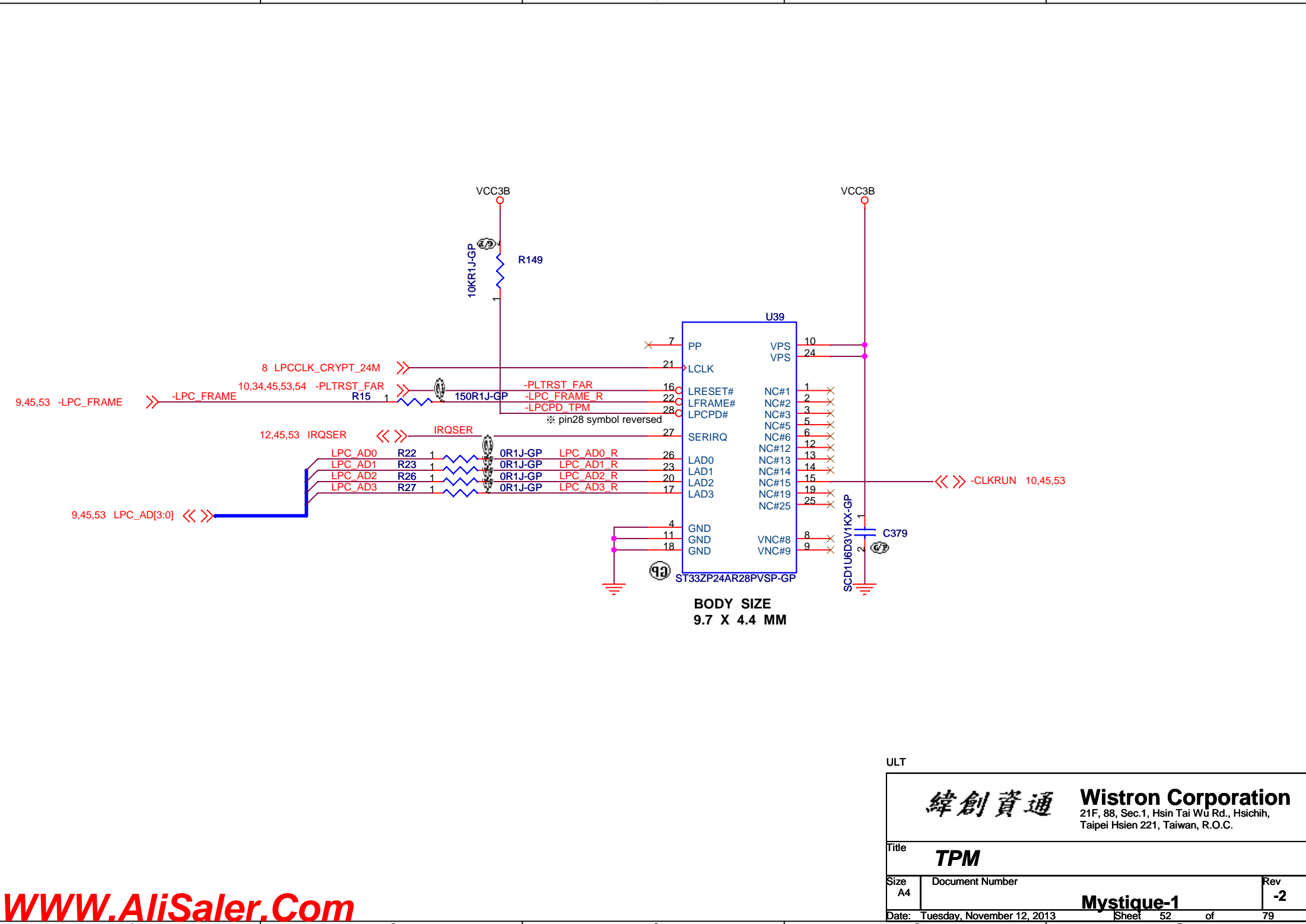
of

79

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		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
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Size A4	Document Number		Rev -2
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Taipei Hsien 221, Taiwan, R.O.C.

Title
TPM

Size
A4 Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

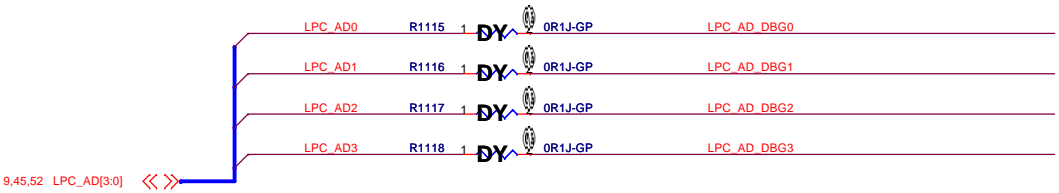
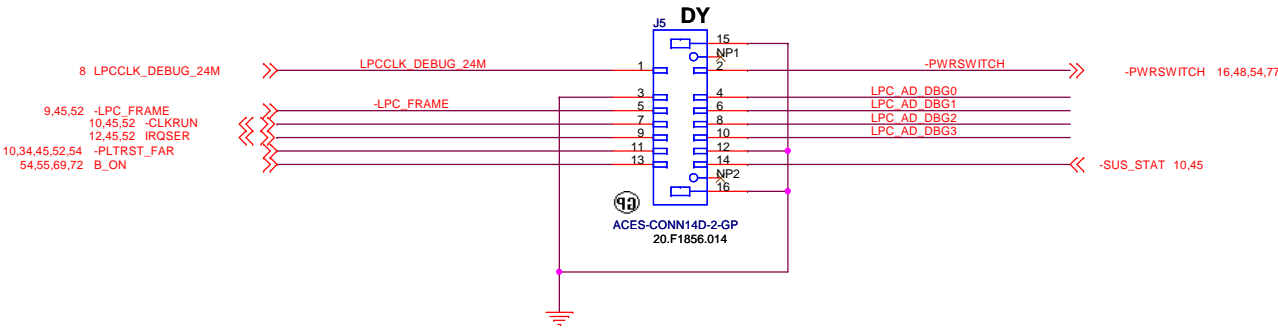
Sheet 52 of 79

TABLE

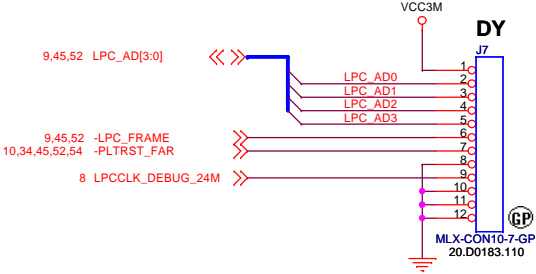
REF DES	ENABLE	DISABLE
J5	ASM	NO_ASM
R220	ASM	NO_ASM
R1115	ASM	NO_ASM
R1116	ASM	NO_ASM
R1117	ASM	NO_ASM
R1118	ASM	NO_ASM

↑
LOGIC

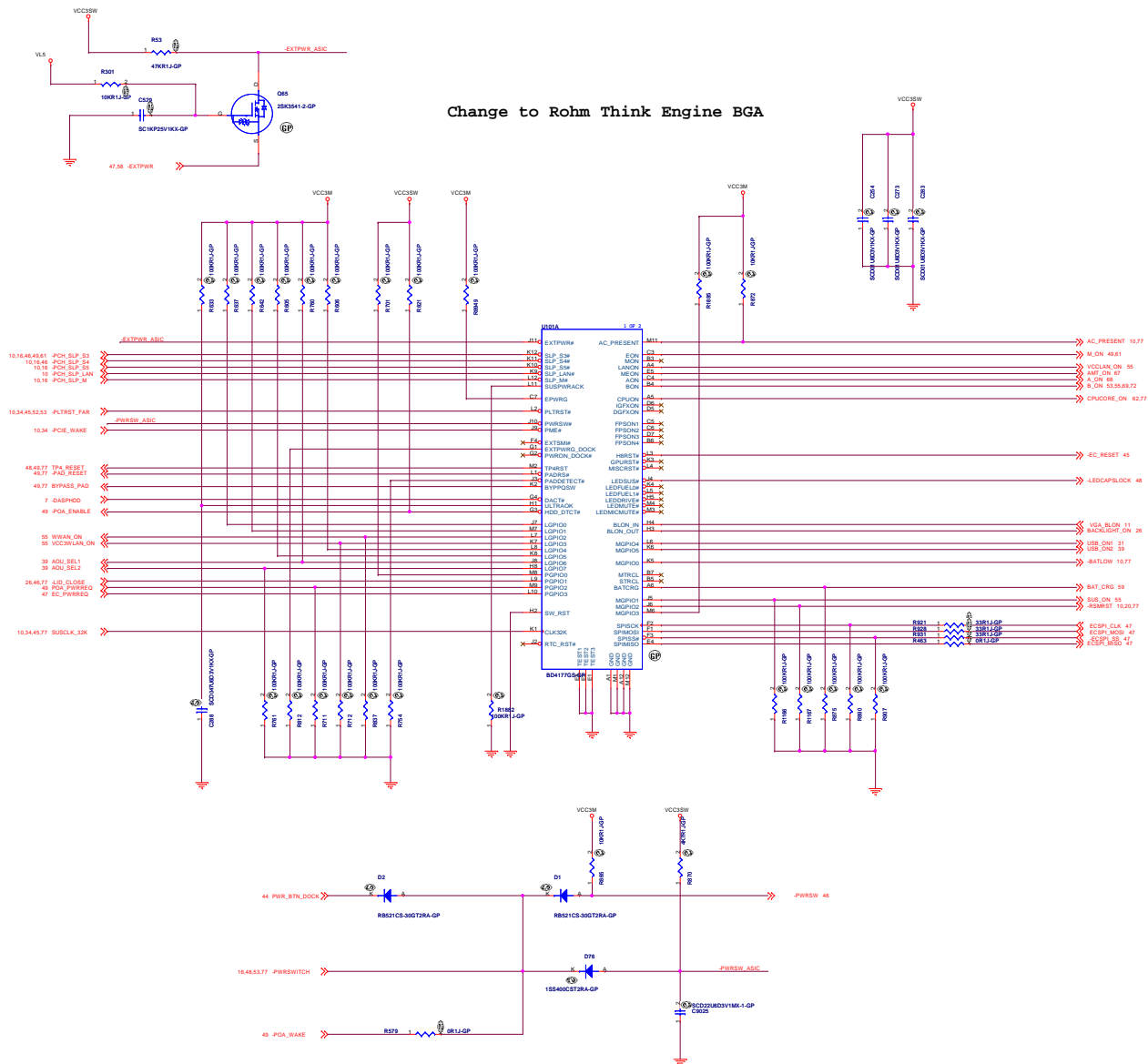
Lenovo Debug Tool I/F

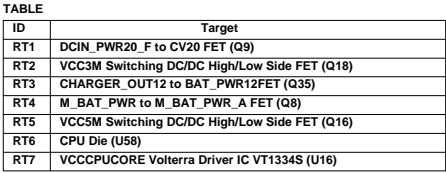


LPC for Debug Card Connector

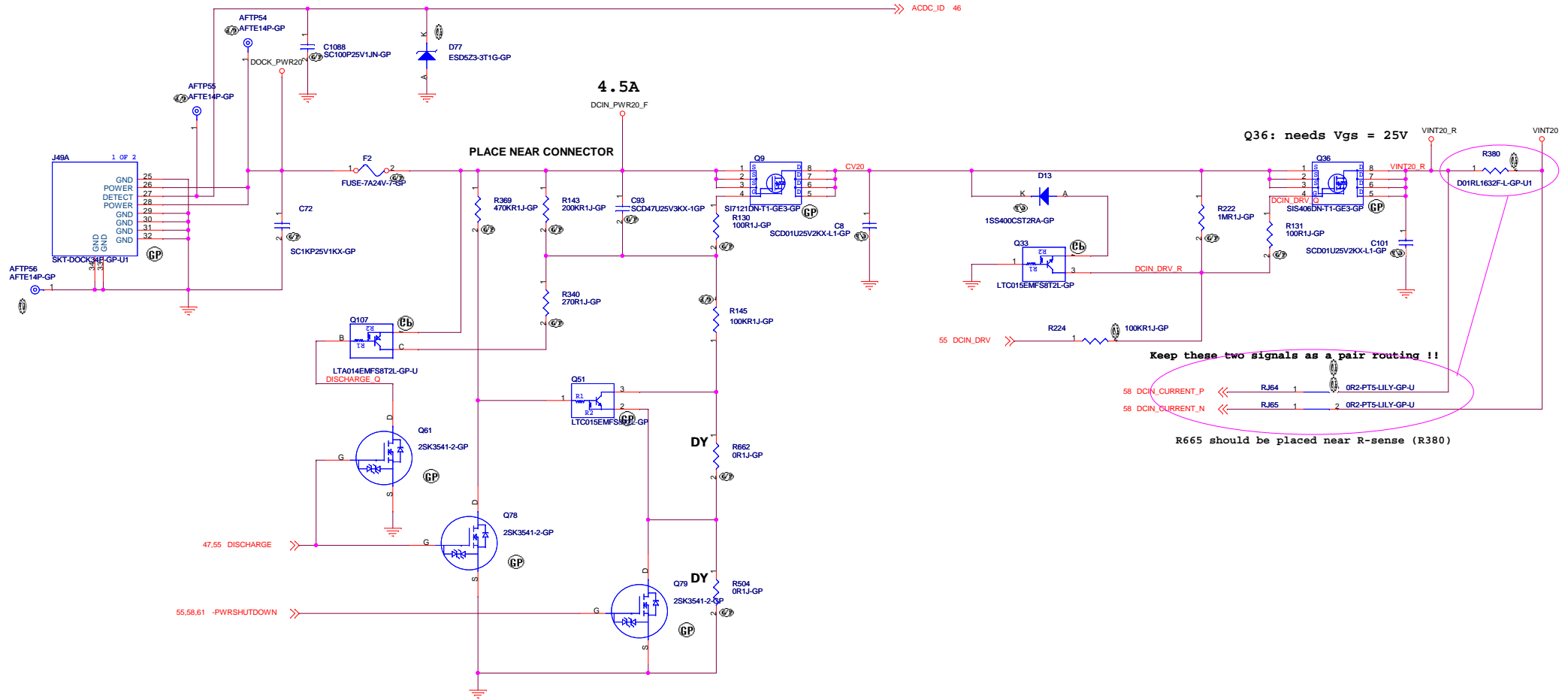


ULT





D77
ESD5Z3.3T1G/On Semi 83.ESD5Z.0AF
uClamp3301H/SEMTECH 83.03301.AAF



TABLE

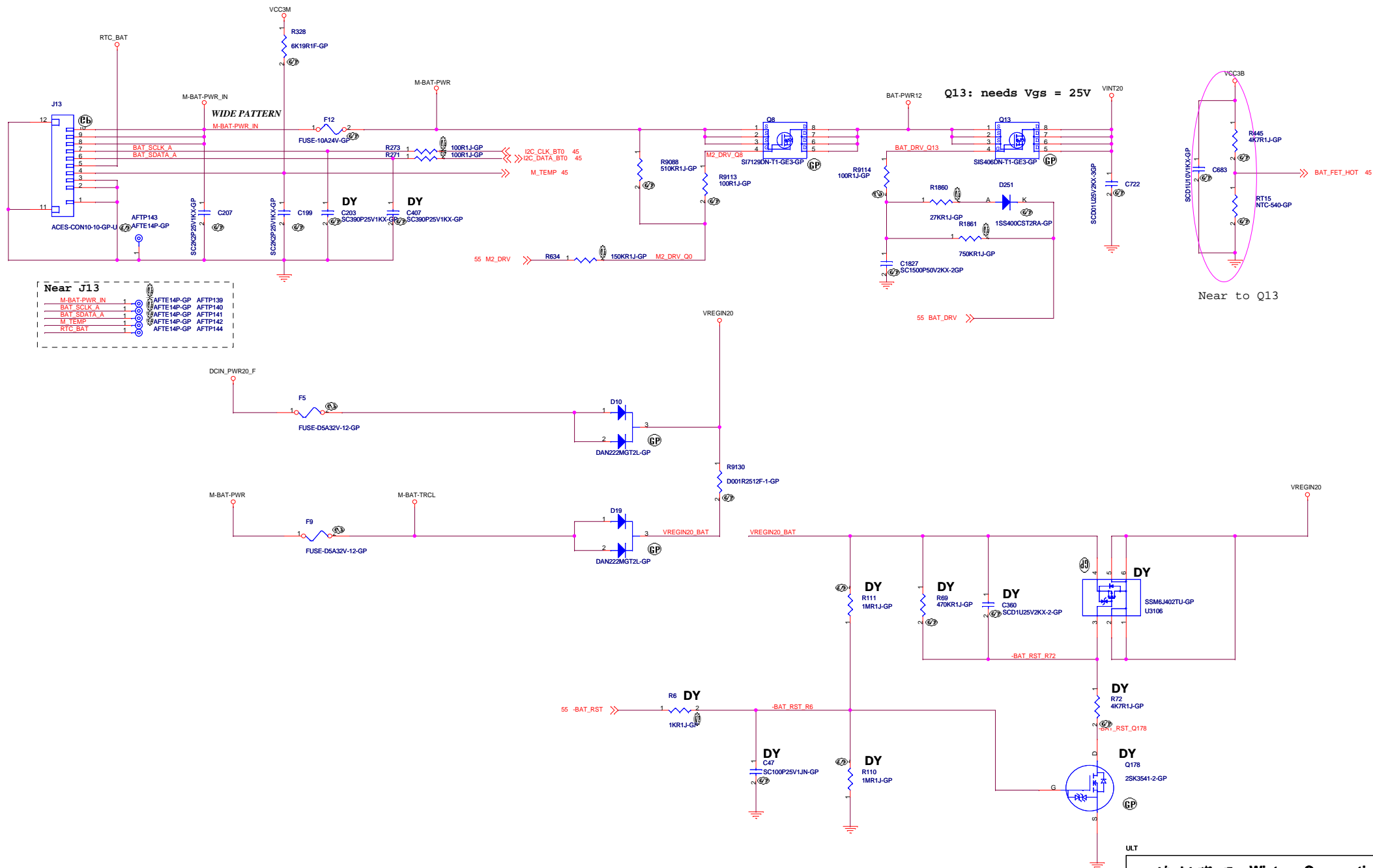
PEAK SHIFT	YES	NO
R662	NO-ASM	ASM
R369	ASM	NO-ASM
Q78	ASM	NO-ASM
Q51	ASM	NO-ASM

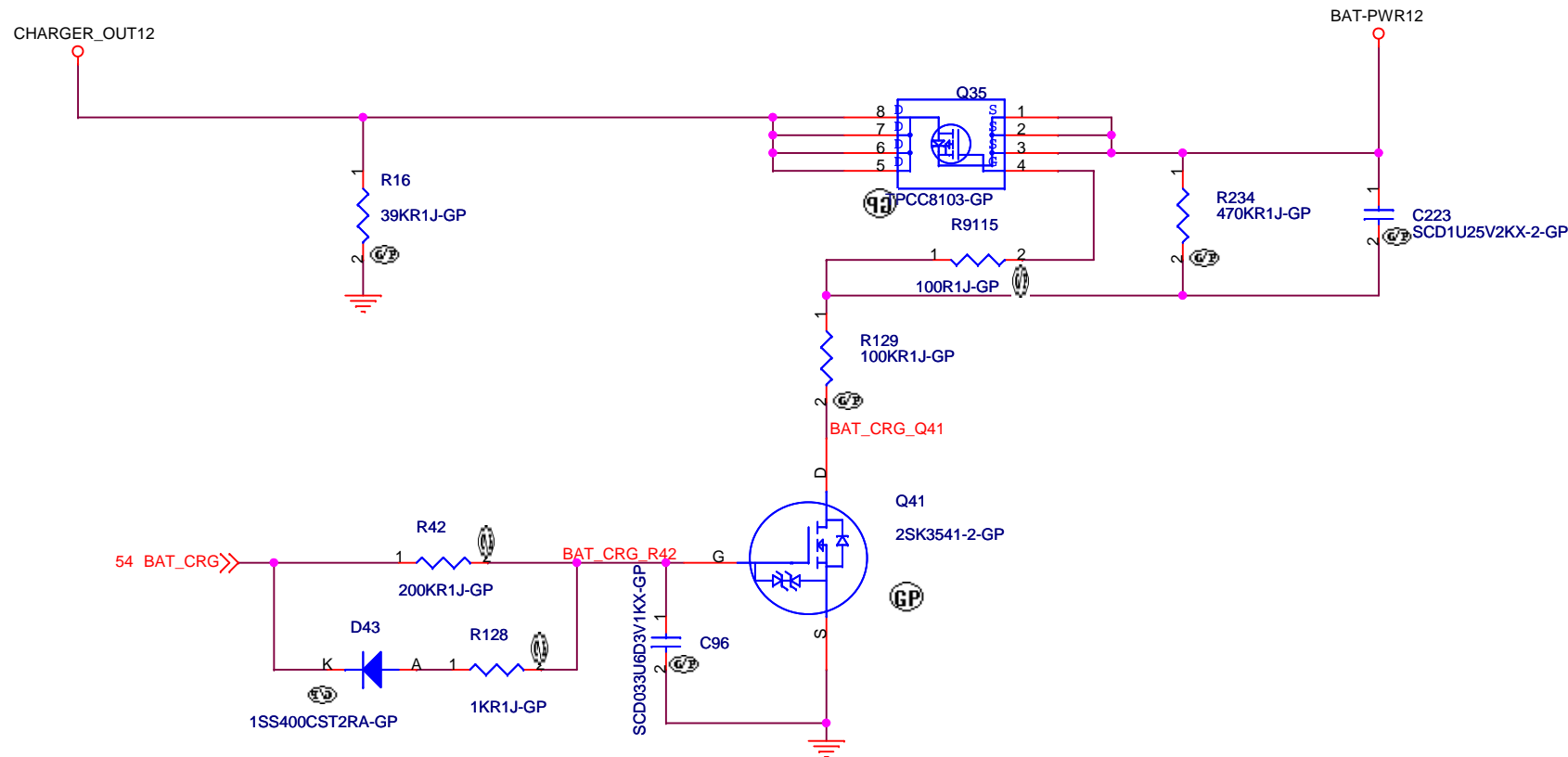
Logic

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File DC-IN	
Size	Document Number
Custom	Mystique-1
Date: Tuesday, November 12, 2013	Sheet 56 of 79

Rev -2





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緯創資通 **Wistron Corporation**
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Taipei Hsien 221, Taiwan, R.O.C.

Title **CHARGER SELECTOR**

Size A4	Document Number Mystique-1	Rev -2
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Date: Tuesday, November 12, 2013 Sheet 59 of 79

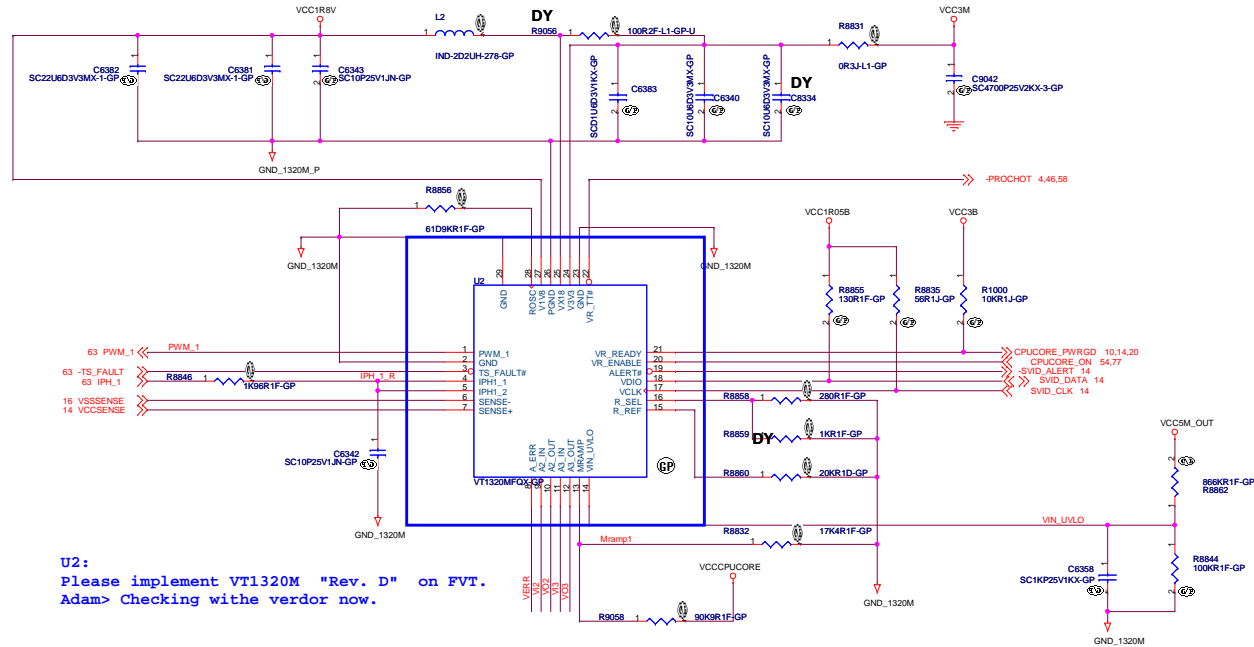


Title

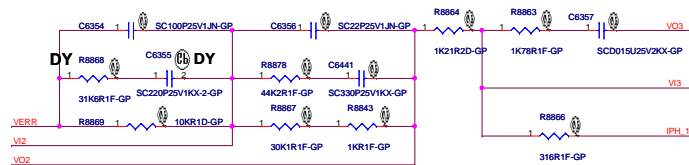
Sheet 60 of 79

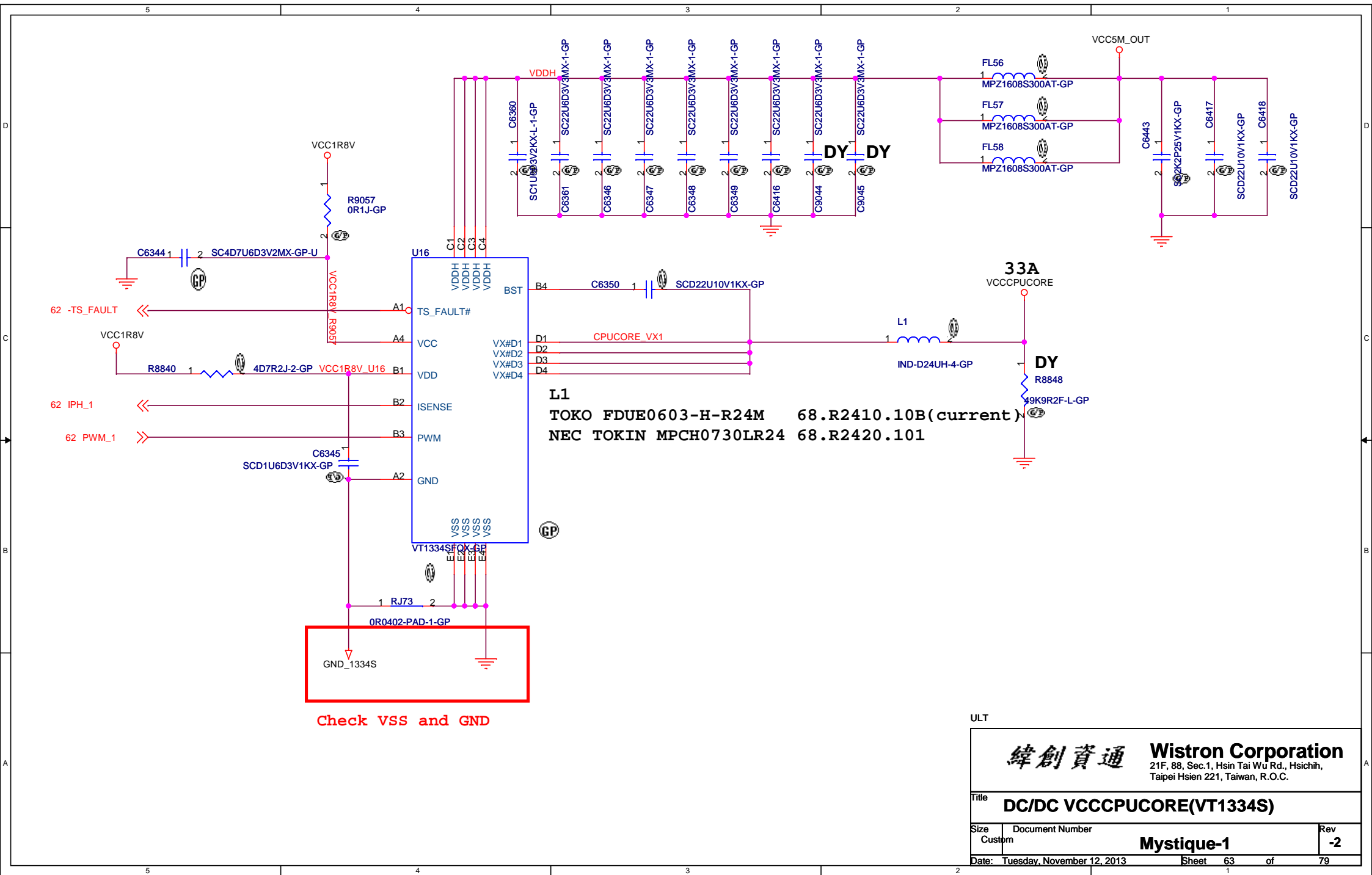
L2
TDK MLP1608V2R2B 68.2R21G.10D
TOKO MDT1608-CH2R2N 68.2R25B.10S

R8831 if noise is visible,
 option to place chip bead instead of resistor to reduce noise



U2:
 Please implement VT1320M "Rev. D" on FVT.
 Adam> Checking with the vendor now.





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Title DC/DC VCCPCUCORE(VT1334S)

Size Document Number
Custom

Mystique-1

Rev
-2

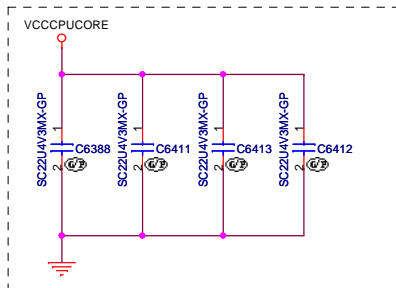
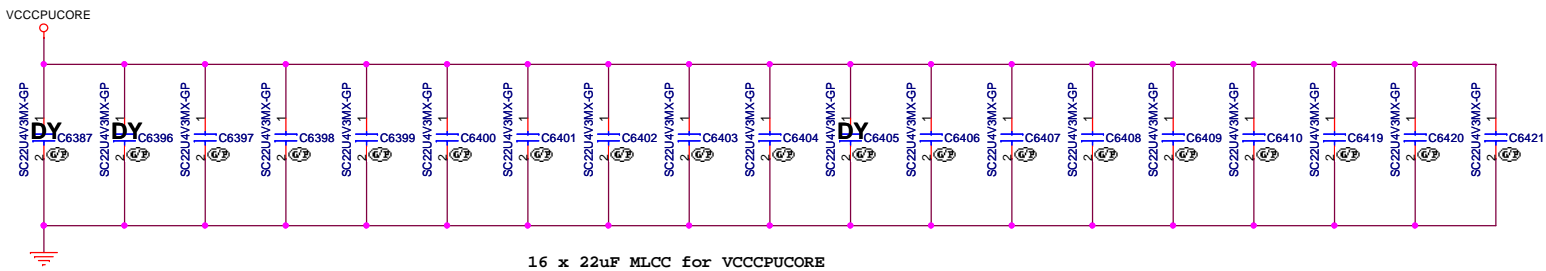
Date: Tuesday, November 12, 2013

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緯創資通			Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.					
Title					
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4 x 22uF MLCC for VCCCPUCORE
 Lenovo: If change from 0805 to 0603 size, the number of capacitors must increased 20% minimum.
 these four caps are the 25% additional.

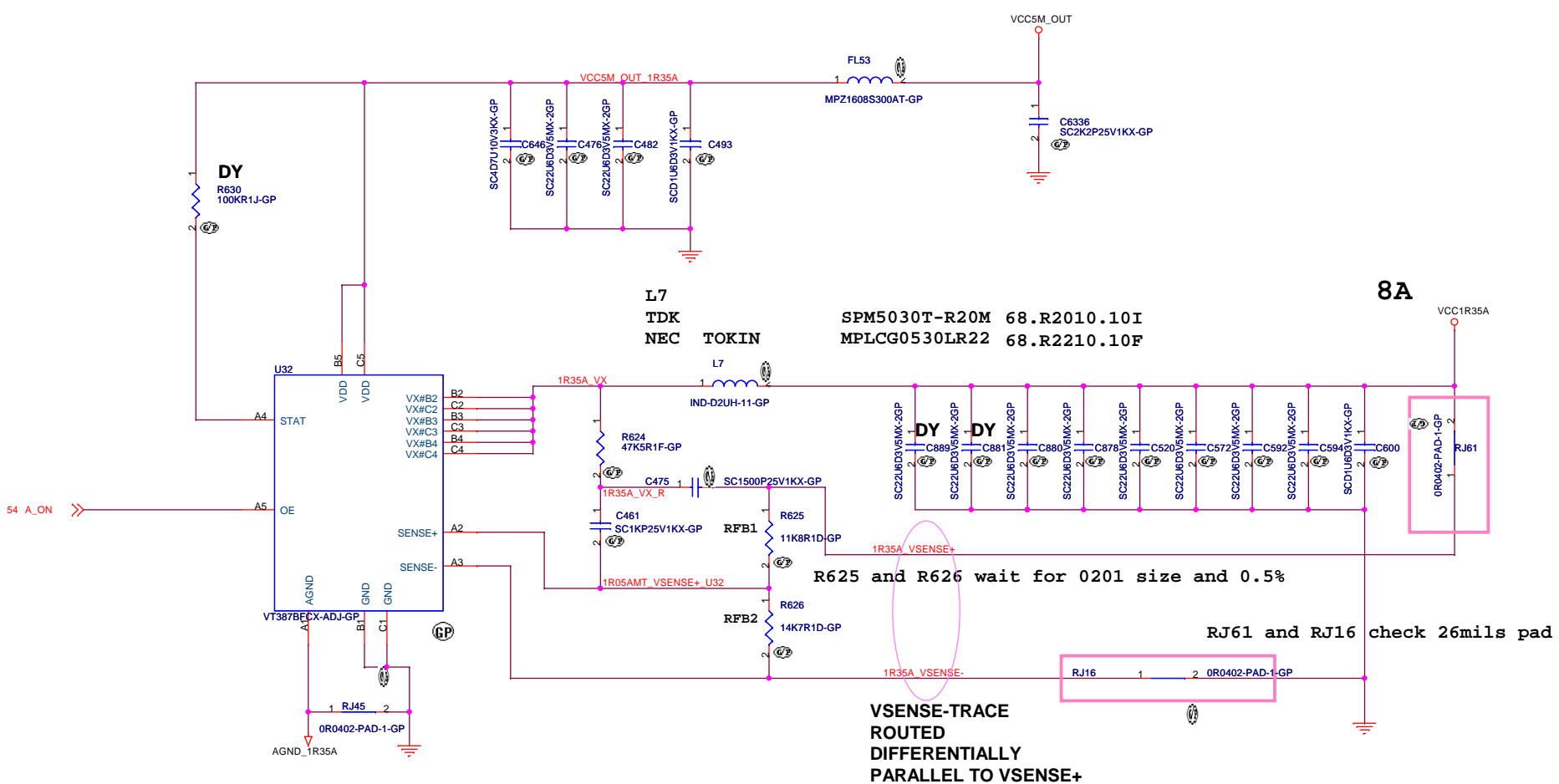
ULT

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title VCCCPUCORE DECOUPLING	
Size A3	Document Number Mystique-1
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Rev -2	

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		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
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SPM5030T-R20M 68.R2010.10I
MPLCG0530LR22 68.R2210.10F

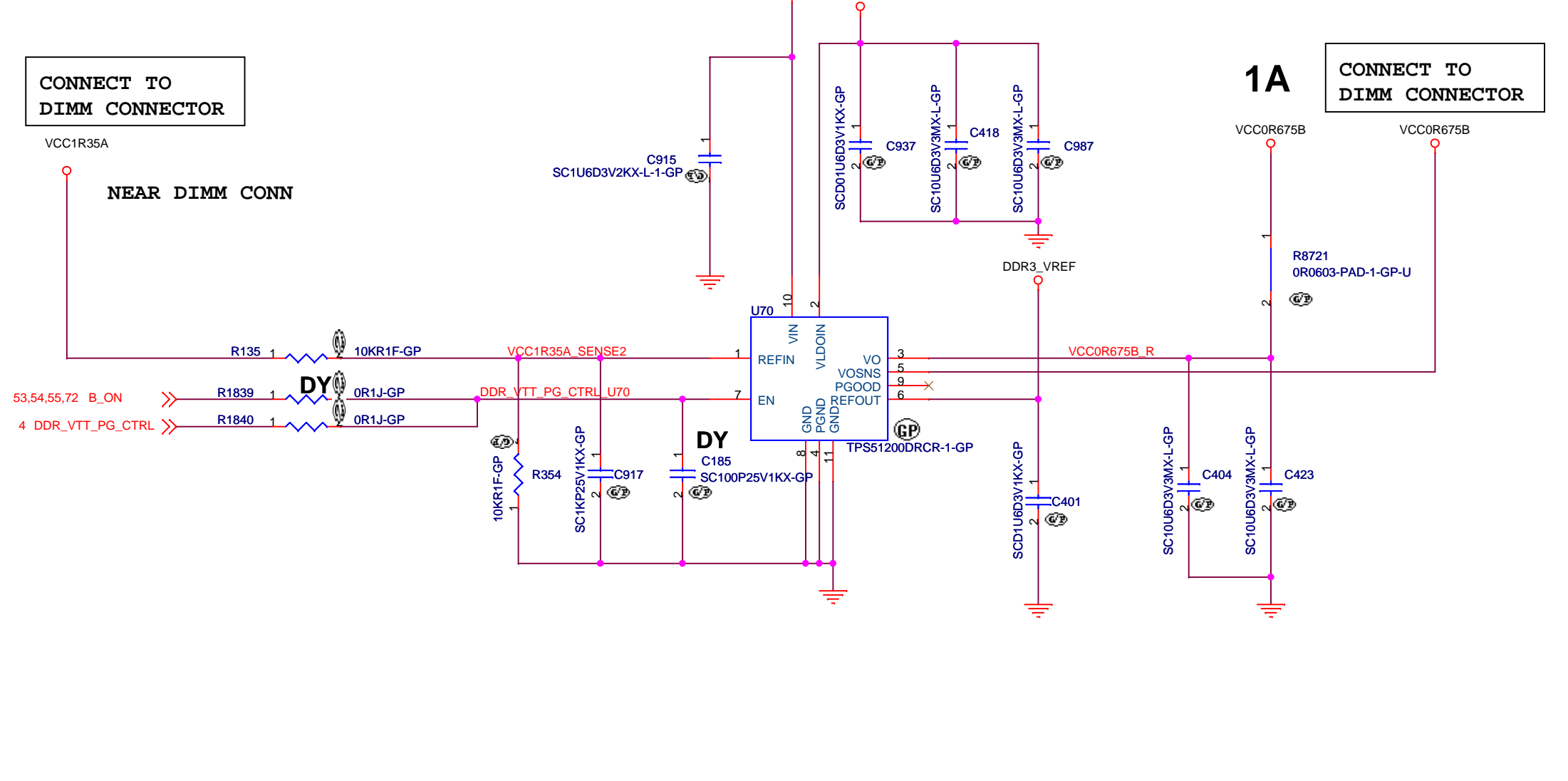
R625 and R626 wait for 0201 size and 0.5%

RJ61 and RJ16 check 26mils pad

VSENSE-TRACE
ROUTED
DIFFERENTIALLY
PARALLEL TO VSENSE+

TABLE

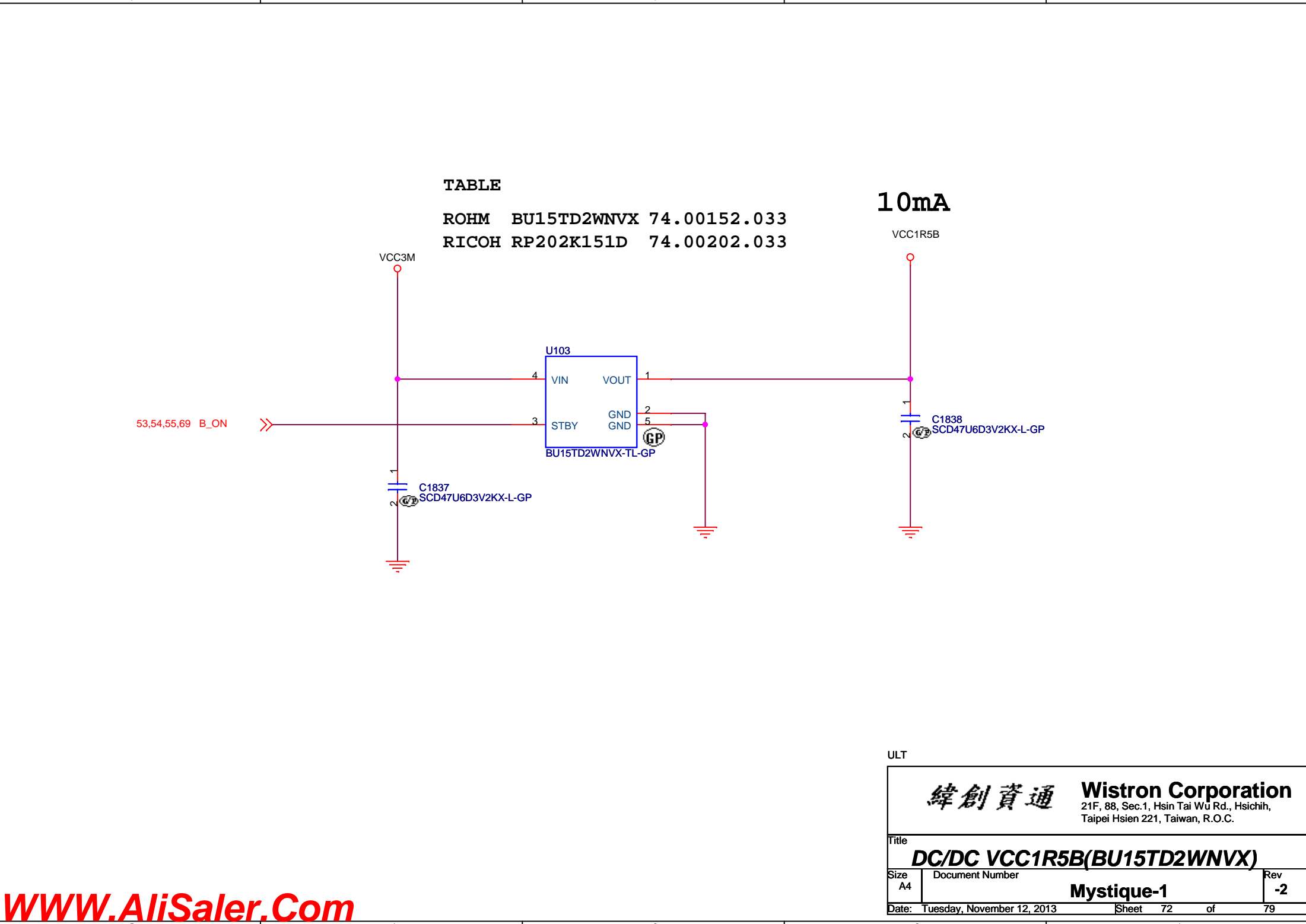
RFB1	RFB2	VOUT
11.8K	14.7K	for 1.35V
13.3K	13.3K	for 1.50V
12.1K	14.7K	for 1.367V
13.3K	13.0K	for 1.517V



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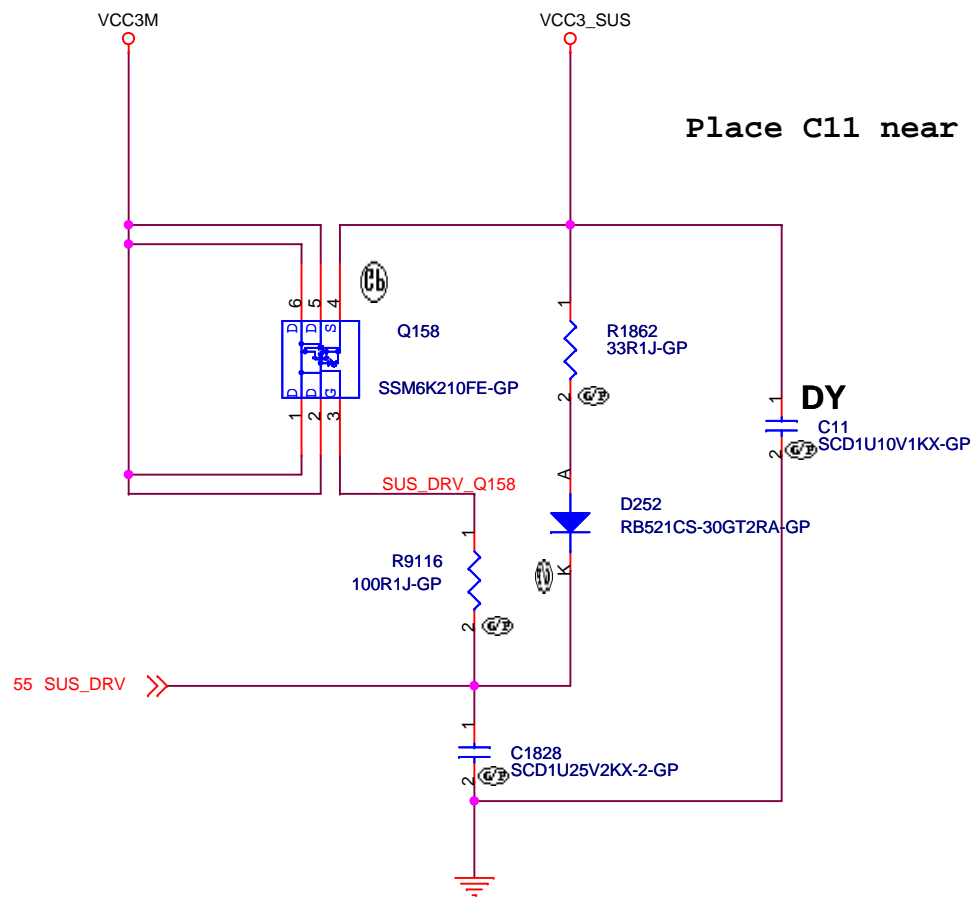
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
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Size	Document Number		Rev
A4	Mystique-1		-2
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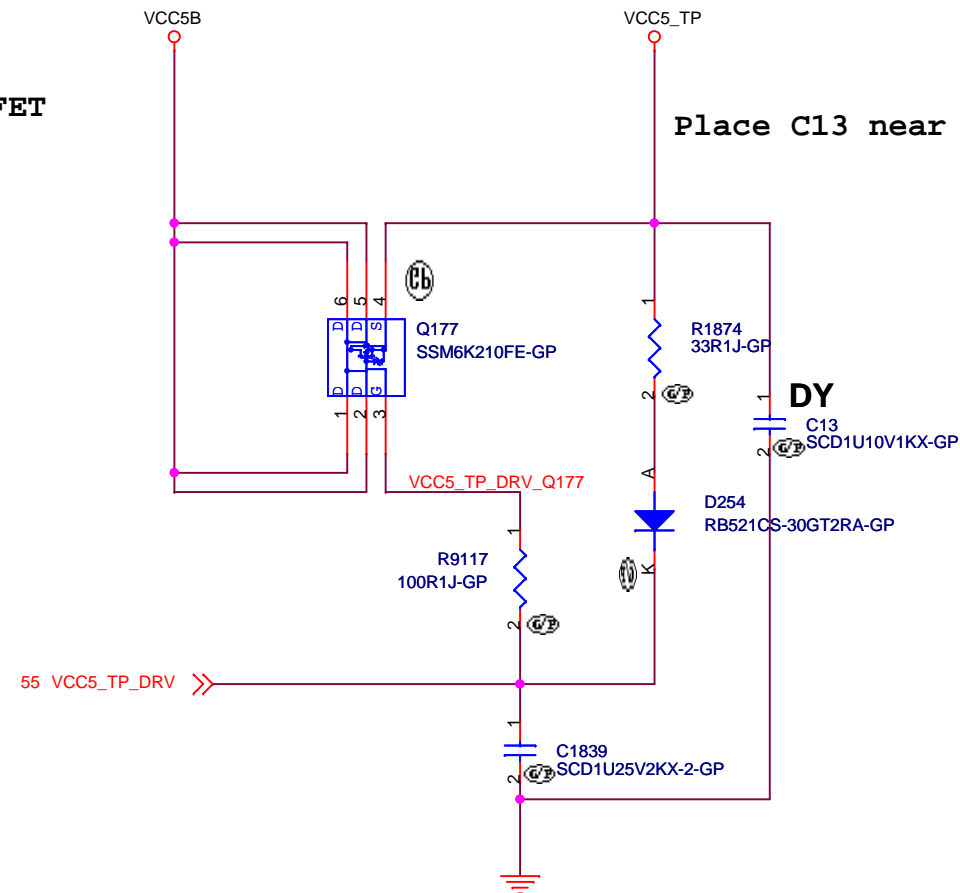
ULT

<div>緯創資通</div>		<div>Wistron Corporation</div>	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DC/DC VCC1R5B(BU15TD2WNVX)			
Size A4	Document Number Mystique-1		Rev -2
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0.3A



15mA



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Taipei Hsien 221, Taiwan, R.O.C.

Title
LOAD SW PCH SUS

Size
A4

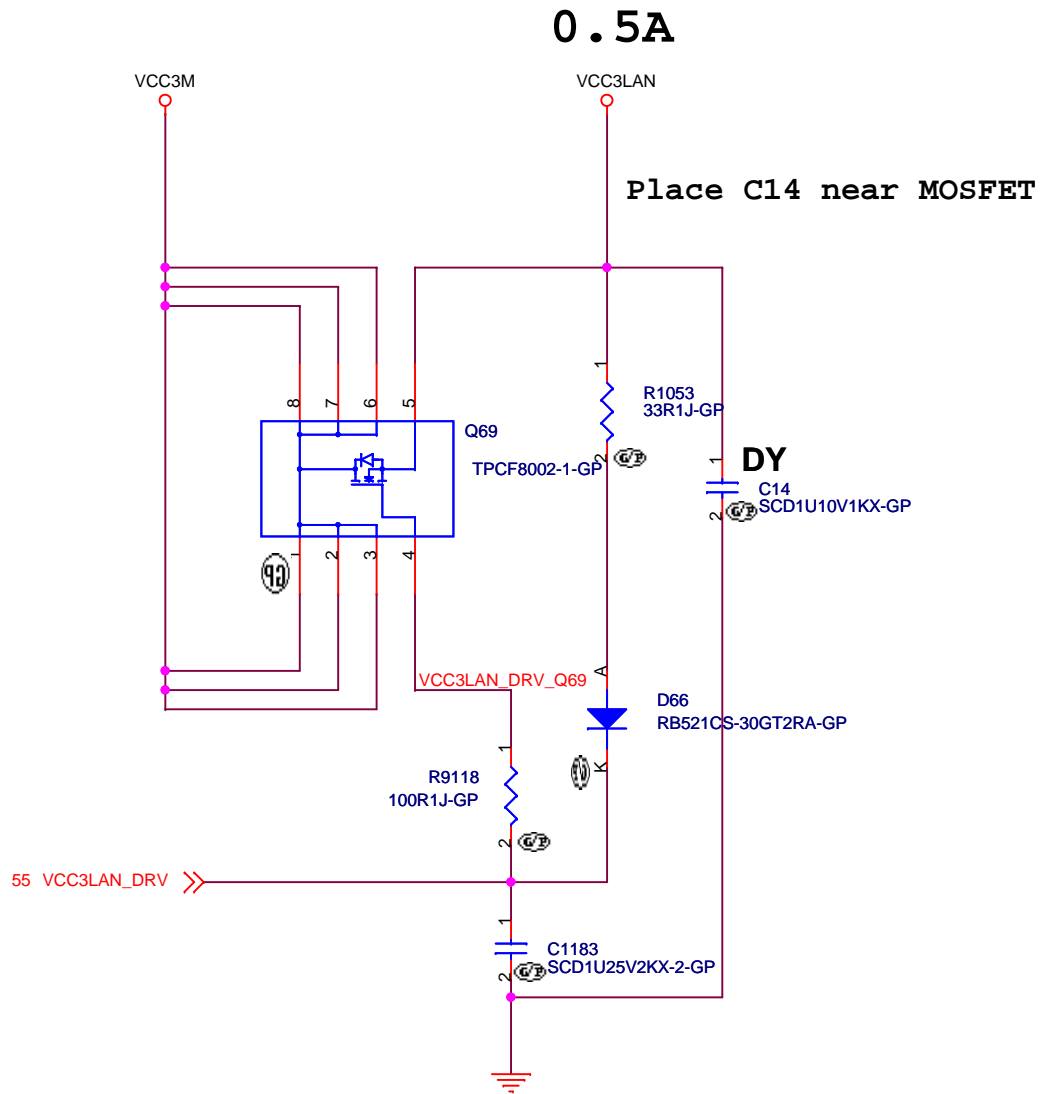
Document Number

Mystique-1

Rev
-2

Date: Tuesday, November 12, 2013

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Taipei Hsien 221, Taiwan, R.O.C.

Title

LOAD SW LAN

Size
A4

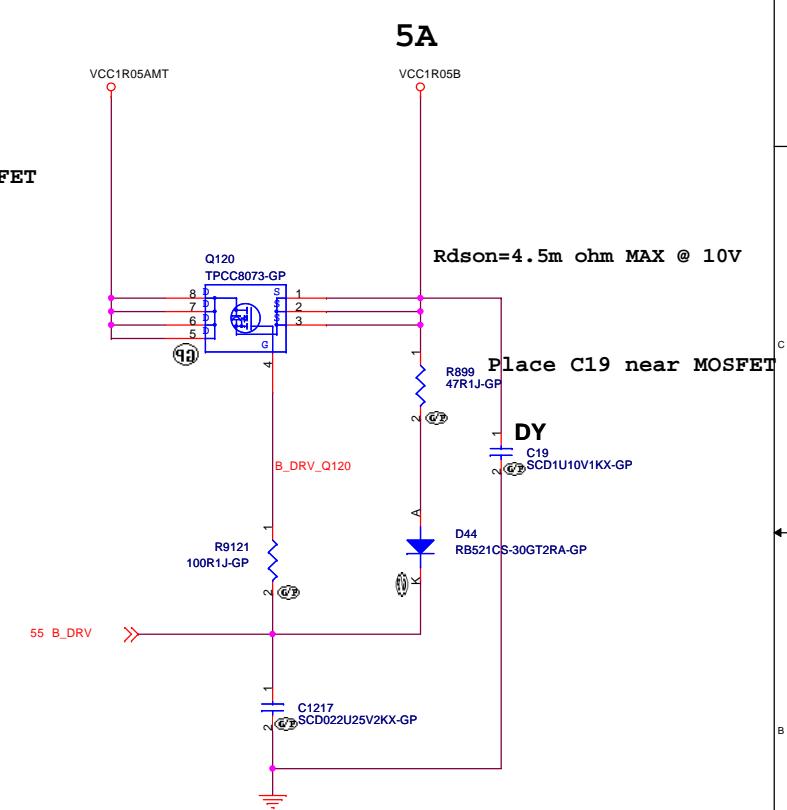
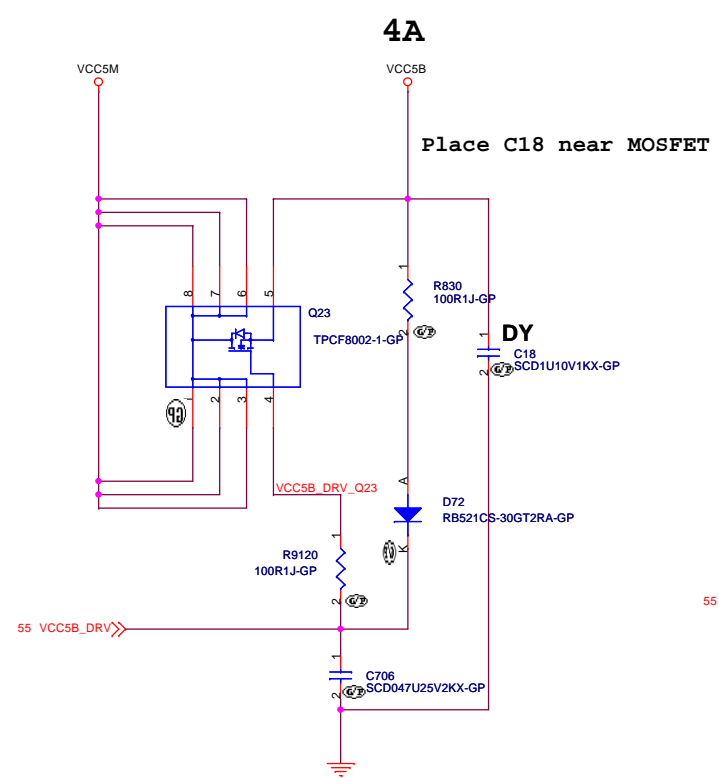
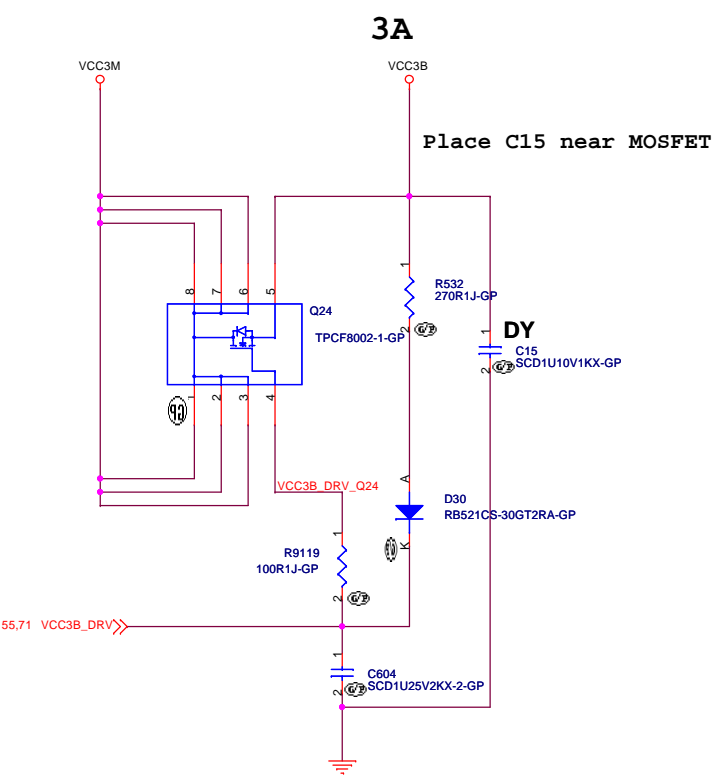
Document Number

Mystique-1

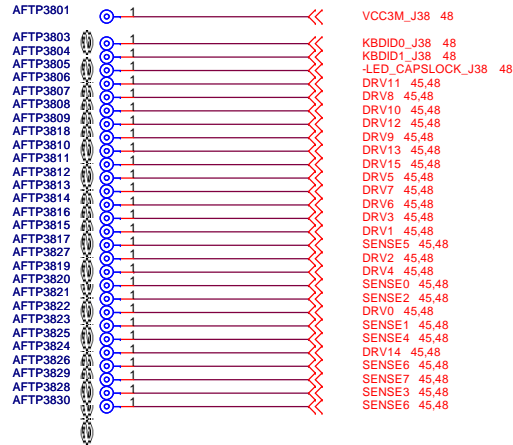
Rev
-2

Date: Tuesday, November 12, 2013

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Near J38(Keyboard connector)



Near J4 (Speaker)



Near J25(SIM)



MFG request

Near J37(Track point connector)



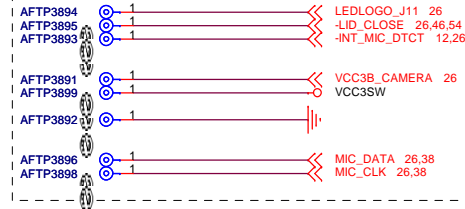
Near J15(FAN Connector)



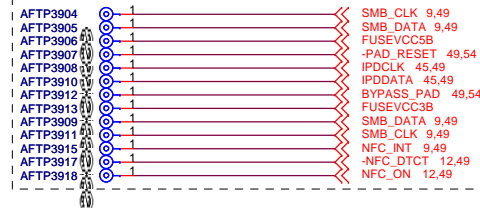
Near J12(RTC)



Near J11(LED)



Near J21 (Clickpad + NFC)



Near S3



For Power Switch

Top side



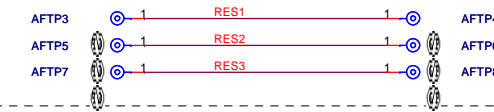
Bottom side



For J48 (DFR)



For PCB resistance measurement when temperature is vary.



ULT

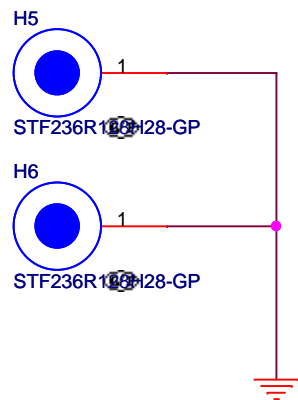
緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title TEST POINT

Size A3	Document Number	Mystique-1	Rev -2
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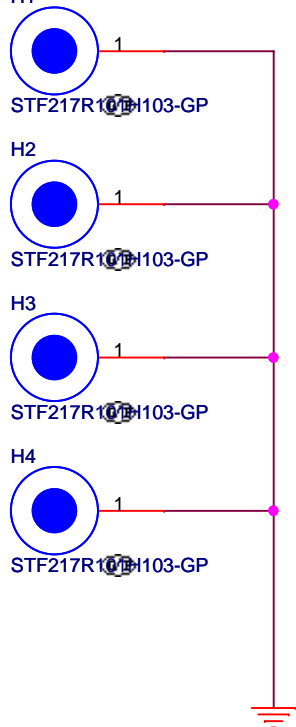
34.4LY01.001

NGFF



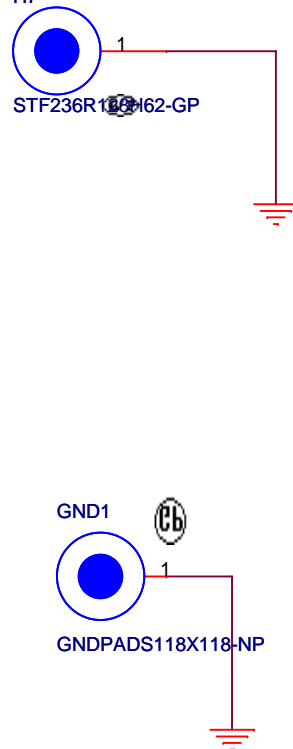
34.4LY02.001

CPU BOSS

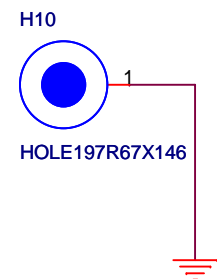


34.4LY03.001

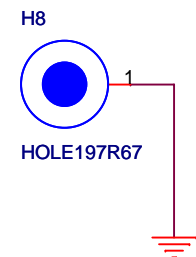
NGFF SSD



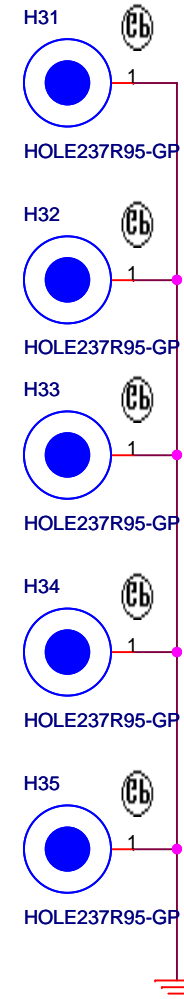
KN1.PAD.01



NL1.PAD.01

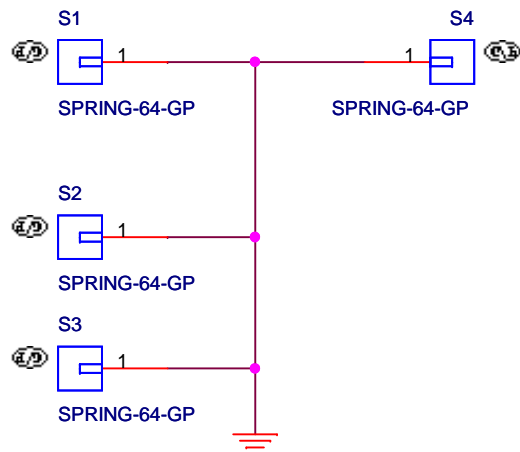


ZZ.00PAD.921



34.4H602.001

mount antenna cable



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Title **HOLES/GND/PADS/TEMP**

Size A Document Number

Mystique-1

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-2

Date: Tuesday, November 12, 2013

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